



JMD100TD18D1W

Three Phase Rectifier Bridge Module

Features

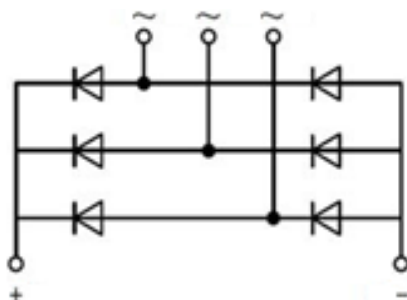
- Low forward voltage and leakage current
- Low inductance package
- High surge current capability

Product Summary

Parameter	Value	Unit
V_{RRM}	1800	V
I_D	100	A
$I_{FSM}(@ t_p = 10ms)$	1100	A
$V_F(Max)$	1.35	V

Applications

- Field supply for DC motors
- Line rectifiers for transistorized AC motor controllers
- Non-controllable rectifiers for AC/DC converter



Absolute Maximum Ratings (@ $T_C = 25^{\circ}C$ unless otherwise specified)

Parameter	Conditions	Symbol	Values	Unit
Repetitive peak reverse voltage		V_{RRM}	1800	V
Non-repetitive peak reverse voltage		V_{RSM}	1900	V
Output current	$T_C = 100^{\circ}C$	I_D	100	A
Forward surge current	1/2 cycle, Sine wave, 50Hz $T_{vj} = 25^{\circ}C$	I_{FSM}	1100	A
I^2t value for fusing		I^2t	6050	A ² s
RMS isolation voltage	A.C 50Hz(1s/1min)	V_{ISO}	3600/3000	V
Junction temperature range		T_J	-40 ~ +150	$^{\circ}C$
Storage temperature range		T_{stg}	-40 ~ +125	$^{\circ}C$

**Electrical Characteristics (@ $T_C = 25^\circ\text{C}$ unless otherwise specified)**

Parameter	Conditions	Symbol	Values			Unit
			Min.	Typ.	Max.	
Forward voltage	$I_F = 100\text{A}$, $T_{vj} = 25^\circ\text{C}$	V_F		1.15	1.35	V
Reverse leakage current	$V_R = V_{RRM}$, $T_{vj} = 25^\circ\text{C}$	I_{RRM}			0.5	mA
	$V_R = V_{RRM}$, $T_{vj} = 150^\circ\text{C}$				10	mA
Threshold voltage	For power loss calculation only $T_{vj} = 150^\circ\text{C}$,	V_{TO}			0.82	V
Dynamic resistance		r_T			3.3	m Ω

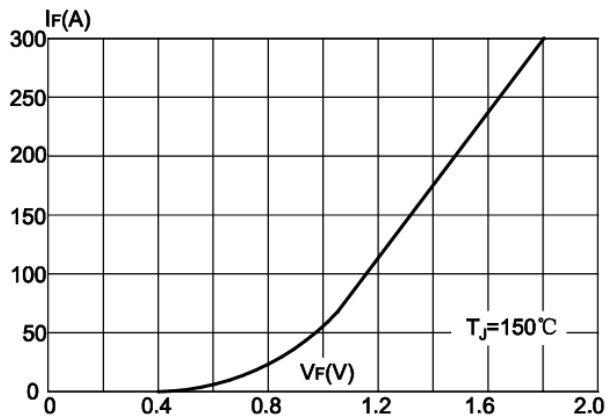
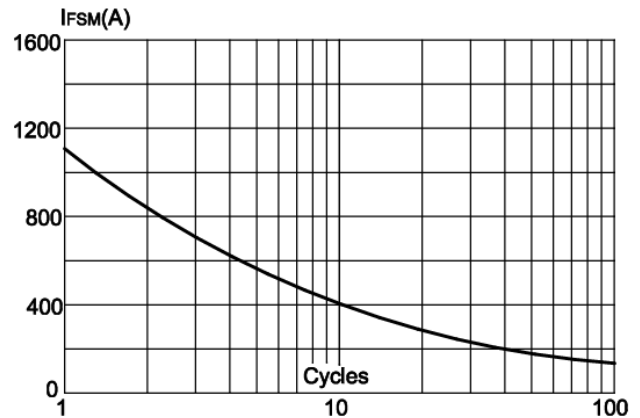
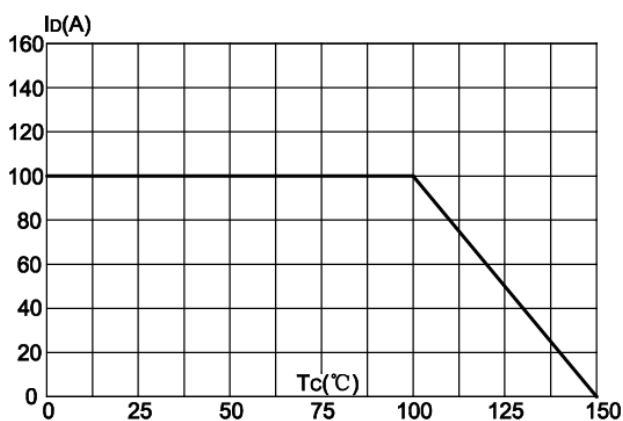
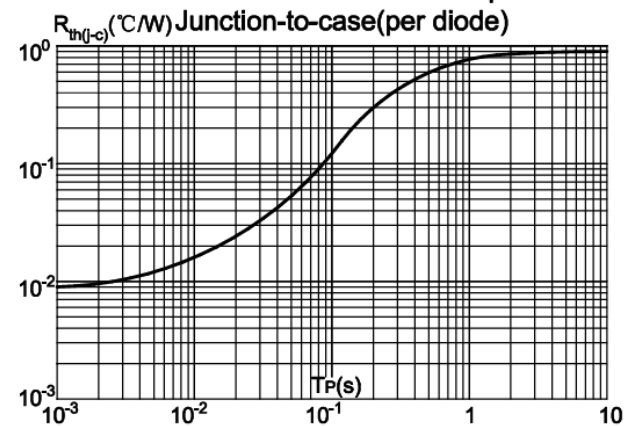
Thermal Characteristics (@ $T_C = 25^\circ\text{C}$ unless otherwise specified)

Parameter	Conditions	Symbol	Values			Unit
			Min.	Typ.	Max.	
Thermal impedance (junction to case)	per diode	$R_{th(j-c)}$			0.9	$^\circ\text{C}/\text{W}$
Mounting torque	Base plate to heatsink screw M5	M	4.25		5.75	N·m
	Electrode to terminal screw M5		4.25		5.75	N·m

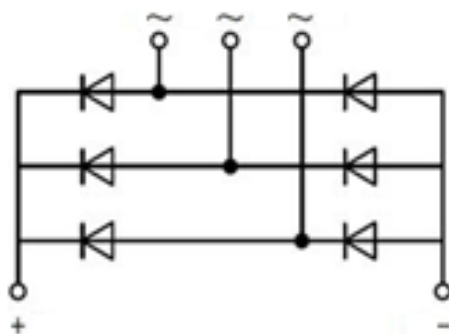
Ordering Information

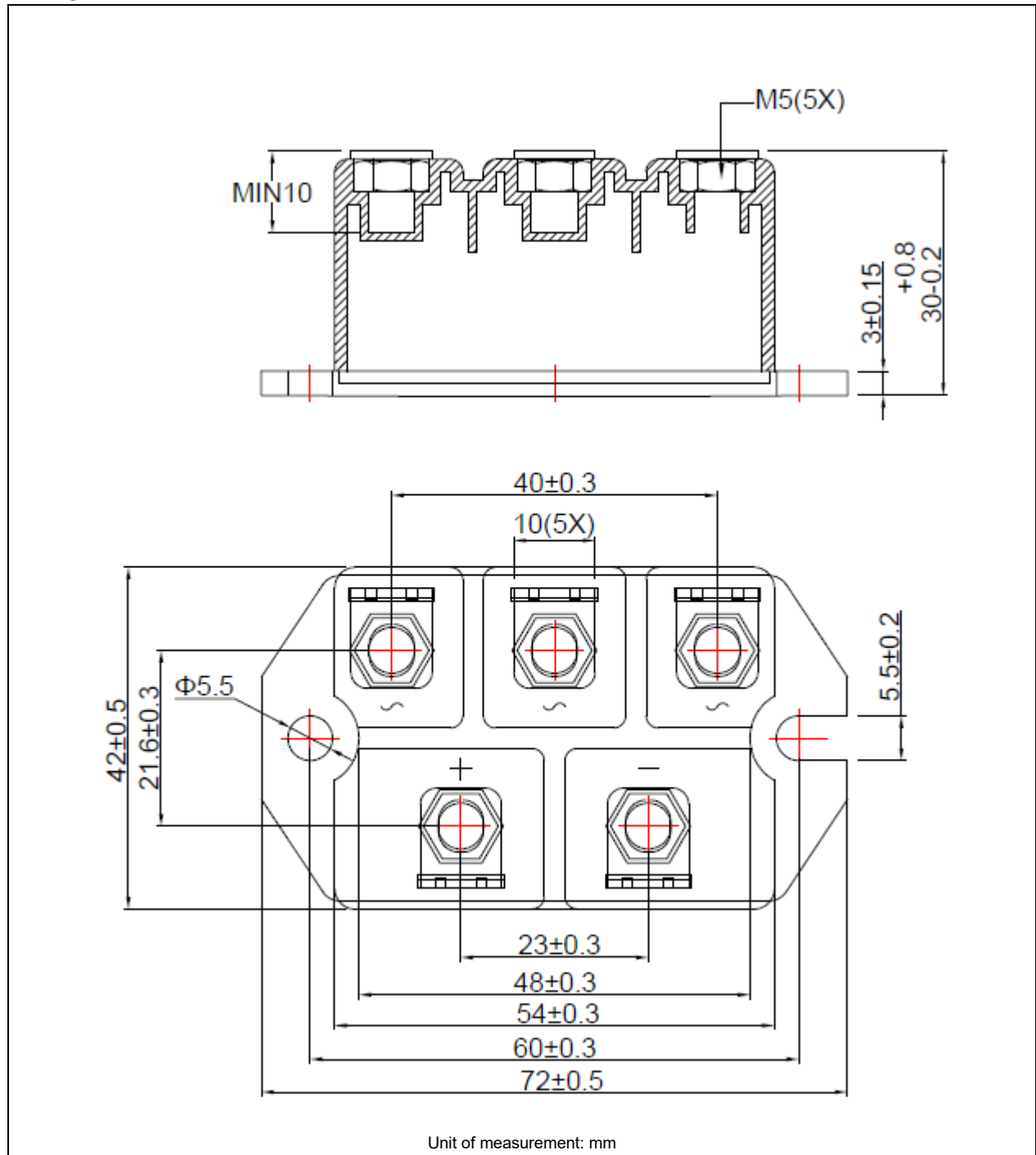
Device	Marking	Package	Weight	Inner Box	Per Carton
JMD100TD18D1W	JMD100TD18D1W	D1	132g/PCS	4PCS	48PCS

Typical Electrical & Thermal Characteristics

FIG.1: Forward characteristics(per diode)

FIG.2: Peak on-state surge current

FIG.3: Forward current vs. case temperature

FIG.4: Maximum transient thermal impedance


Circuit Diagram



Package Outlines (mm)




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