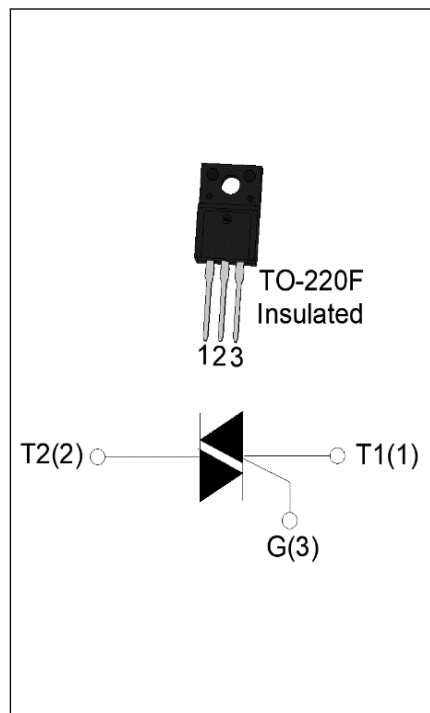


JST24F-800C 25A TRIAC

Rev.A.1.1

DESCRIPTION:

The JST24F-800C triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. By using an external plastic package, JST24F-800C provides a rated insulation voltage of 2000 VRMS, complying with UL standards (File ref: E252906). Package TO-220F is RoHS compliant.



MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	25	A
V_{DRM}/V_{RRM}	800	V
$I_{GT\ I/II/III/IV}$	25/25/25/50	mA

ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Storage junction temperature range		T _{stg}	-40-150	℃
Operating junction temperature range		T _j	-40-125	℃
Repetitive peak off-state voltage (T _j =25℃)		V _{DRM}	800	V
Repetitive peak reverse voltage (T _j =25℃)		V _{RRM}	800	V
RMS on-state current (T _c ≤72℃)		I _{T(RMS)}	25	A
Non repetitive surge peak on-state current (full cycle , t _p =20ms , T _j =25℃)		I _{TSM}	250	A
Non repetitive surge peak on-state current (full cycle , t _p =16.6ms , T _j =25℃)			275	
I ² t value for fusing (t _p =10ms , T _j =25℃)		I ² t	340	A ² s
Critical rate of rise of on-state current (I _G =2×I _{GT} , f=100Hz , T _j =125℃)	I - II	di/dt	80	A/μs
	III-IV		40	
Peak gate current (t _p =20μs , T _j =125℃)		I _{GM}	4	A
Average gate power dissipation (T _j =125℃)		P _{G(AV)}	0.5	W
Peak gate power		P _{GM}	10	W

Peak pulse voltage ($T_j=25^{\circ}\text{C}$; non-repetitive, off-state; FIG.7)	V_{pp}	2	kV
--	----------	---	----

ELECTRICAL CHARACTERISTICS ($T_j=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12\text{V}$ $R_L=33\Omega$	I - II - III	MAX.	25	mA
		IV		50	
V_{GT}		ALL	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125^{\circ}\text{C}$ $R_L=3.3\text{k}\Omega$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	I - III - IV	MAX.	70	mA
		II		100	
I_H	$I_T=500\text{mA}$		MAX.	60	mA
dV/dt	$V_D=540\text{V}$ Gate Open $T_j=125^{\circ}\text{C}$		MIN.	500	$\text{V}/\mu\text{s}$
$(dV/dt)_c$	$(dI/dt)_c=13.3\text{A/ms}$, $T_j=125^{\circ}\text{C}$		MIN.	6	$\text{V}/\mu\text{s}$
t_{on}	$I_G=80\text{mA}$ $I_A=400\text{mA}$ $I_R=40\text{mA}$ $T_j=25^{\circ}\text{C}$		TYP.	3	μs
t_{off}				50	

STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=35\text{A}$ $t_p=380\mu\text{s}$	$T_j=25^{\circ}\text{C}$	1.5	V
V_{TO}	Threshold voltage	$T_j=125^{\circ}\text{C}$	0.75	V
R_D	Dynamic resistance	$T_j=125^{\circ}\text{C}$	18	$\text{m}\Omega$
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25^{\circ}\text{C}$	5	μA
I_{RRM}		$T_j=125^{\circ}\text{C}$	2	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	1.5	$^{\circ}\text{C}/\text{W}$
$R_{th(j-a)}$	junction to ambient (AC)	60	$^{\circ}\text{C}/\text{W}$

ORDERING INFORMATION

J	ST	24	F	-800	C
JieJie Microelectronics Co., Ltd.	Triacs	$I_{T(RMS)}:25A$			$C:I_{GT1-3}\leq 25mA\ I_{GT4}\leq 50mA$
		F:TO-220F(Ins)		800: $V_{DRM}/V_{RRM}\geq 800V$	

MARKING

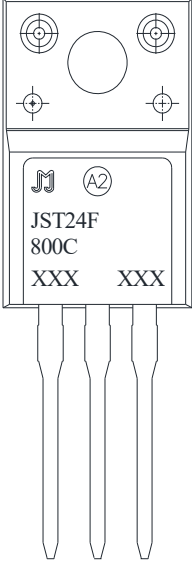
	XXX XXX Year _____ Month _____ Production Code _____
---	---

FIG.1: Maximum power dissipation versus RMS on-state current

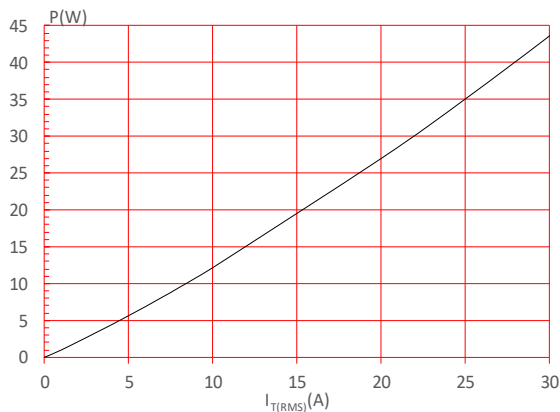


FIG.3: Surge peak on-state current versus number of cycles

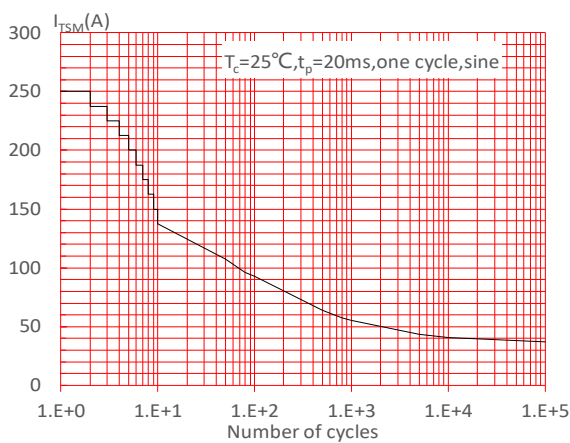


FIG.5: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20\text{ms}$, and corresponding value of I^2t (I - II: $di/dt < 80\text{A}/\mu\text{s}$; III-IV: $di/dt < 40\text{A}/\mu\text{s}$)

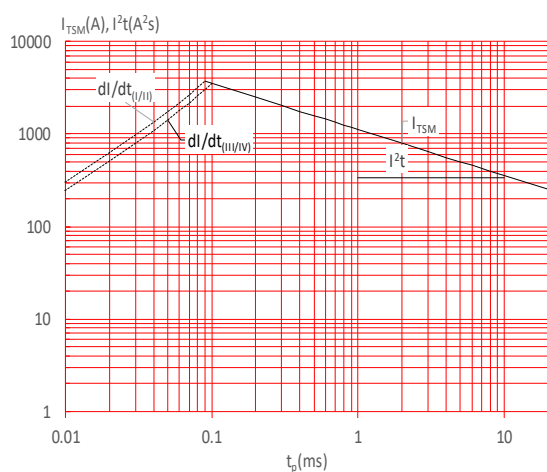


FIG.2: RMS on-state current versus case temperature

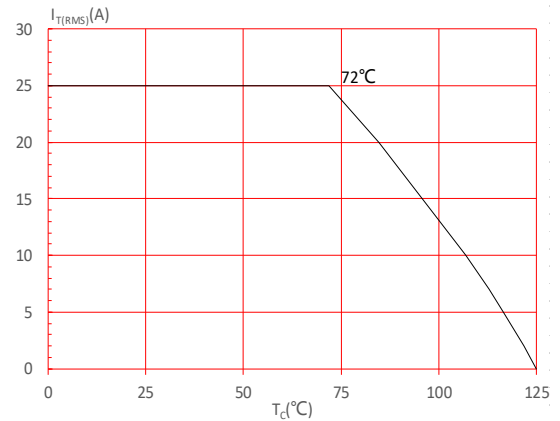


FIG.4: On-state characteristics

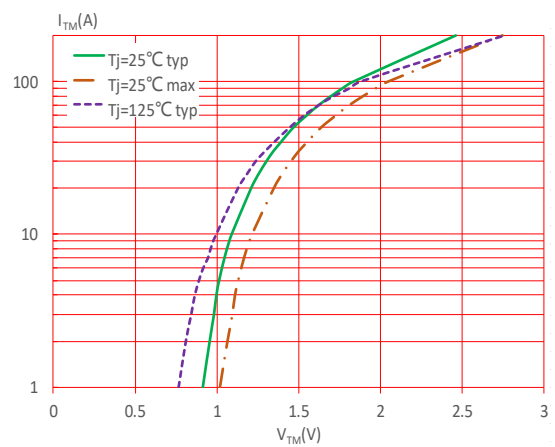


FIG.6: Relative variations of gate trigger current, holding current and latching current versus junction temperature

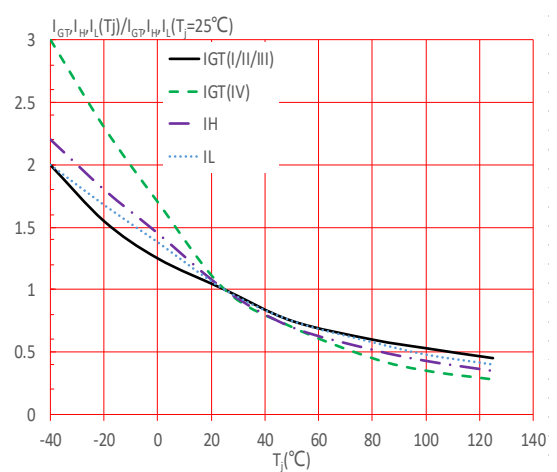
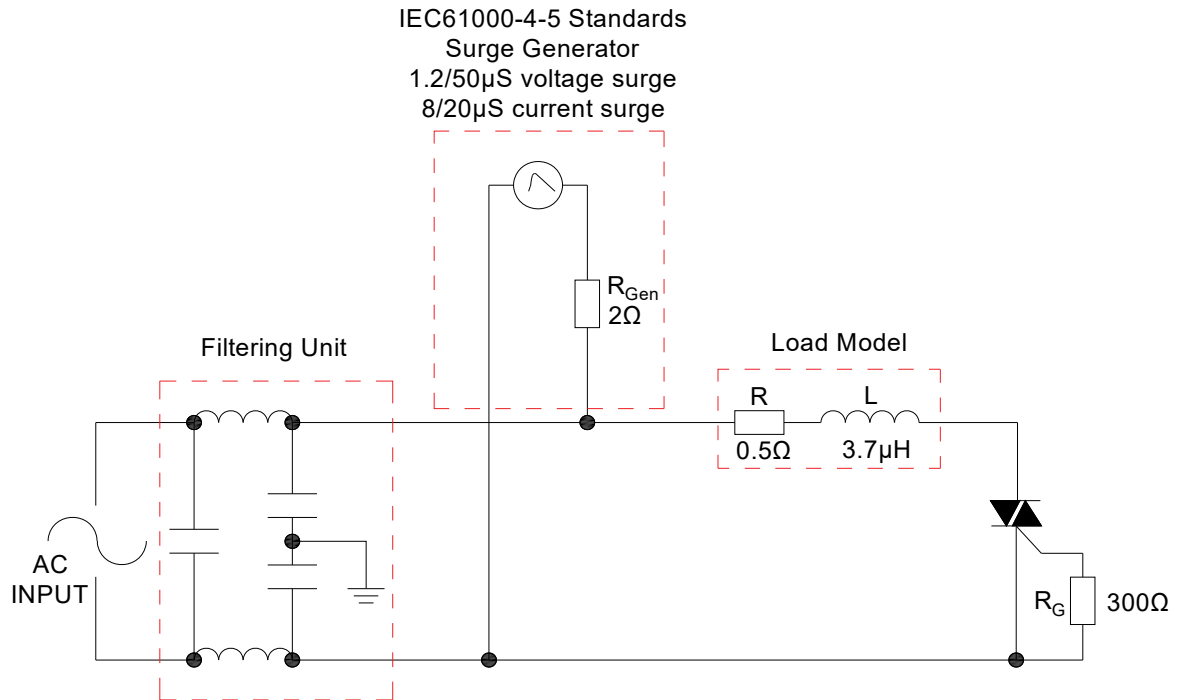


FIG.7: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie Microelectronics.

ORDERING INFORMATION

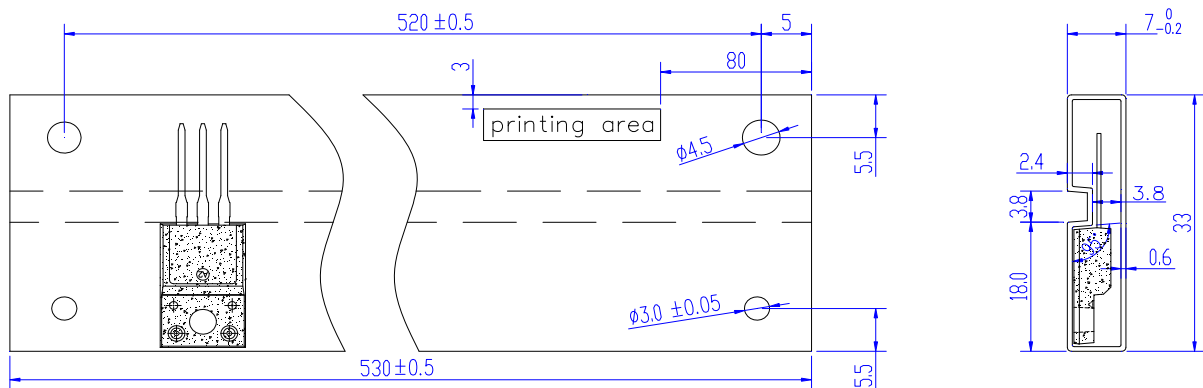
Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		I - II - III	IV			
JST24F-800C	800	25	50	TO-220F(Ins)	50	Tube

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Oct.14, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

Technical drawing of a 3-pronged plug (Fig. 1). The drawing includes a top view and a side view. The top view shows a rectangular body with a central circular hole and two side holes. The side view shows the profile of the plug with three prongs. Dimensions are indicated by blue lines and labels: A (total height), C2 (height of the top section), F (height of the middle section), D (height of the bottom section), C3 (height of the bottom section), C (width of the base), B (width of the body), M (width of the prongs), G (width of the prongs), K (height of the prongs), L1 (height of the prongs), L2 (width of the prongs), L3 (width of the body), I (width of the top section), E (width of the body), and J (height of the body). A note indicates a hole diameter of $\phi 3.05-3.25\text{mm}$.

DELIVERY MODE



<http://www.jjwdz.com>

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.

 is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.
Copyright © 2025 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.