

JST24C-600B 25A TRIAC

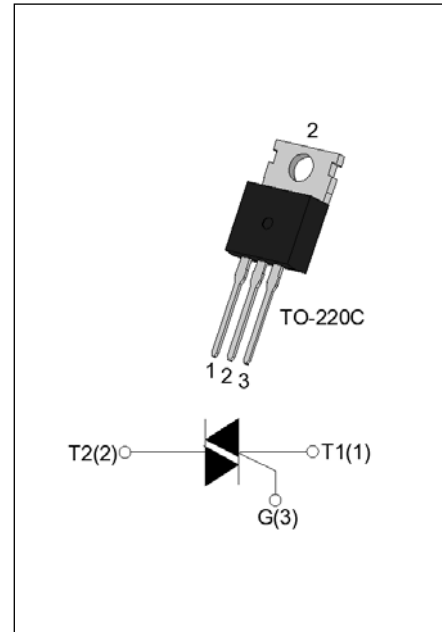
Rev.A.1.1

DESCRIPTION:

The JST24C-600B triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. From T2 terminals to external heatsink. Package TO-220C is RoHS compliant.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	25	A
V_{DRM}/V_{RRM}	600	V
$I_{GT\ I}/II/III/IV$	50/50/50/70	mA


ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Storage junction temperature range		T _{stg}	-40-150	℃
Operating junction temperature range		T _j	-40-125	℃
Repetitive peak off-state voltage (T _j =25℃)		V _{DRM}	600	V
Repetitive peak reverse voltage (T _j =25℃)		V _{RRM}	600	V
RMS on-state current (T _c ≤100℃)		I _{T(RMS)}	25	A
Non repetitive surge peak on-state current (full cycle , t _p =20ms , T _j =25℃)		I _{TSM}	250	A
Non repetitive surge peak on-state current (full cycle , t _p =16.6ms , T _j =25℃)			275	
I ² t value for fusing (t _p =10ms , T _j =25℃)		I ² t	340	A ² s
Critical rate of rise of on-state current (I _G =2×I _{GT} , f=100Hz , T _j =125℃)	I - II	di/dt	100	A/μs
	III-IV		50	
Peak gate current (t _p =20μs , T _j =125℃)		I _{GM}	4	A
Average gate power dissipation (T _j =125℃)		P _{G(AV)}	0.5	W
Peak gate power		P _{GM}	10	W
Peak pulse voltage (T _j =25℃; non-repetitive,off-state;FIG.7)		V _{pp}	2	kV

ELECTRICAL CHARACTERISTICS ($T_j=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12\text{V } R_L=33\Omega$	I - II - III	MAX.	50	mA
		IV		70	
V_{GT}		ALL	MAX.	1	V
V_{GD}	$V_D=V_{DRM} \ T_j=125^{\circ}\text{C}$ $R_L=3.3\text{k}\Omega$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	I - III - IV	MAX.	80	mA
		II		120	
I_H	$I_T=500\text{mA}$		MAX.	80	mA
dV/dt	$V_D=400\text{V}$ Gate Open $T_j=125^{\circ}\text{C}$		MIN.	1200	$\text{V}/\mu\text{s}$
$(dV/dt)_c$	$(dI/dt)_c=13.3\text{A/ms}$, $T_j=125^{\circ}\text{C}$		MIN.	12	$\text{V}/\mu\text{s}$
t_{on}	$I_G=80\text{mA } I_A=400\text{mA } I_R=40\text{mA}$ $T_j=25^{\circ}\text{C}$		TYP.	3	μs
t_{off}				50	

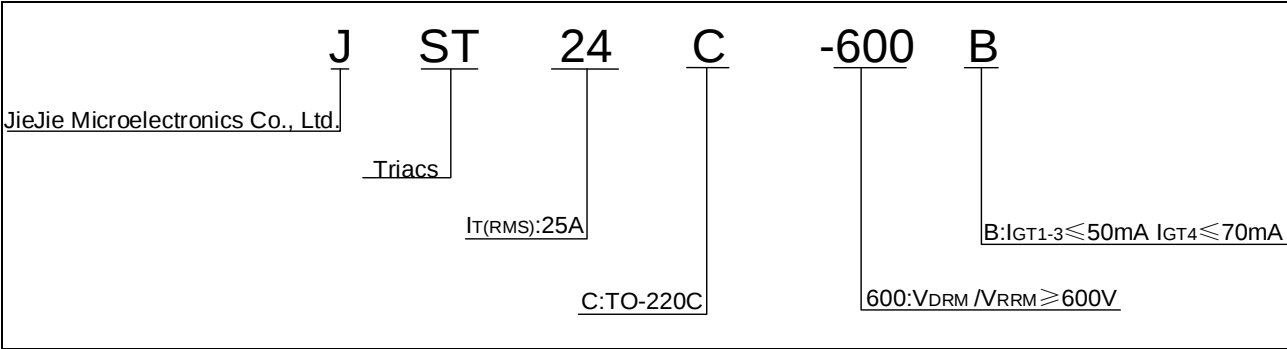
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=35\text{A } t_p=380\mu\text{s}$	$T_j=25^{\circ}\text{C}$	1.5	V
V_{TO}	Threshold voltage	$T_j=125^{\circ}\text{C}$	0.75	V
R_D	Dynamic resistance	$T_j=125^{\circ}\text{C}$	18	$\text{m}\Omega$
I_{DRM}	$V_D=V_{DRM} \ V_R=V_{RRM}$	$T_j=25^{\circ}\text{C}$	5	μA
I_{RRM}		$T_j=125^{\circ}\text{C}$	1.5	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	0.7	$^{\circ}\text{C}/\text{W}$
$R_{th(j-a)}$	junction to ambient (AC)	60	$^{\circ}\text{C}/\text{W}$

ORDERING INFORMATION



MARKING

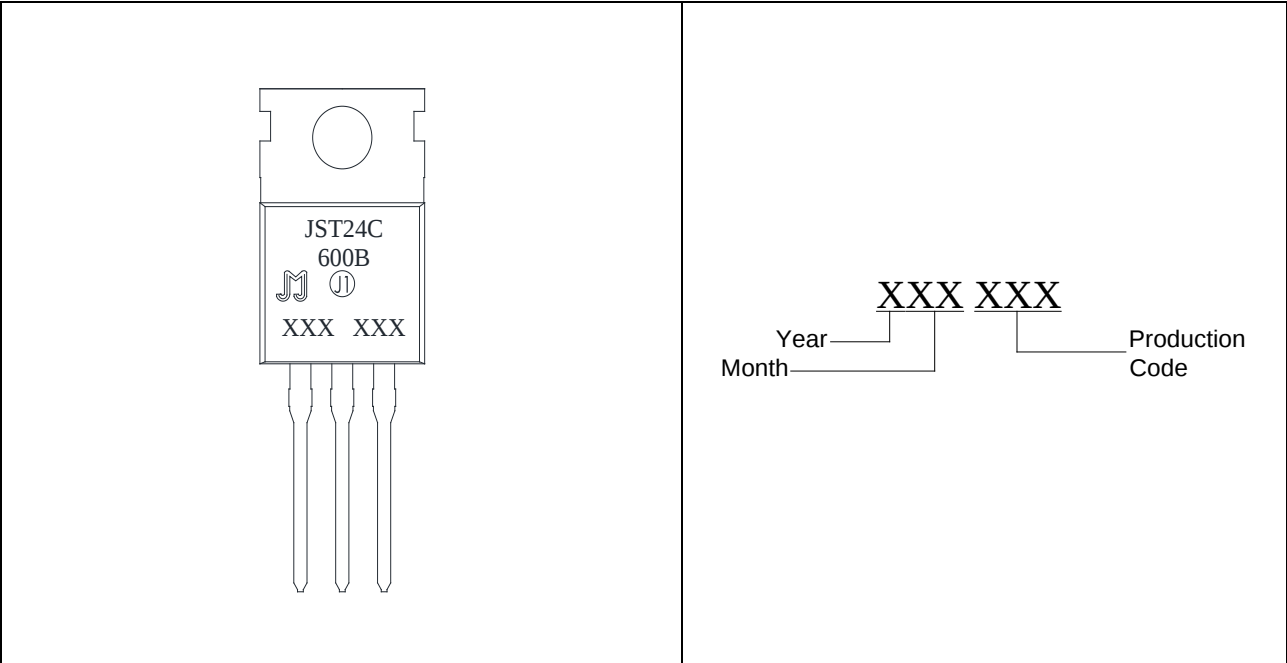


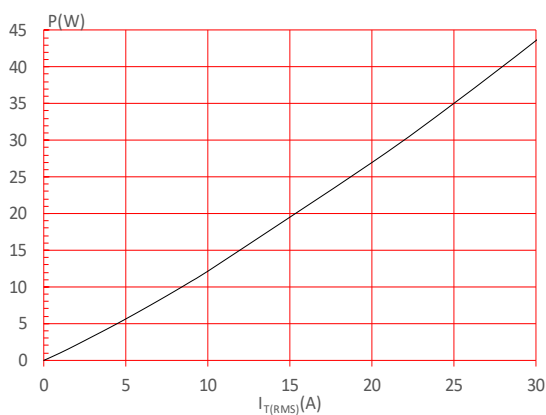
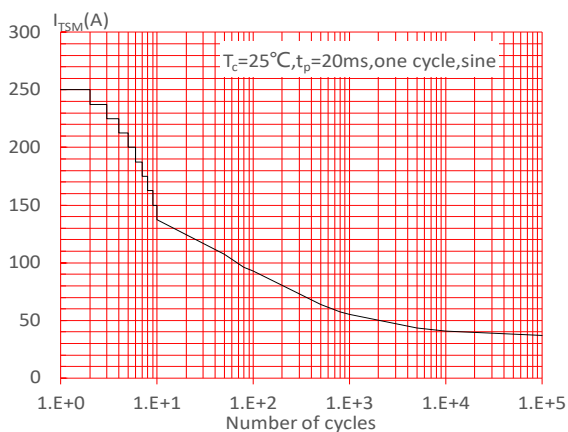
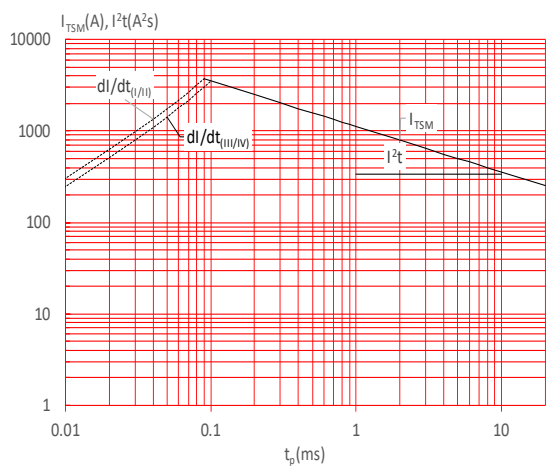
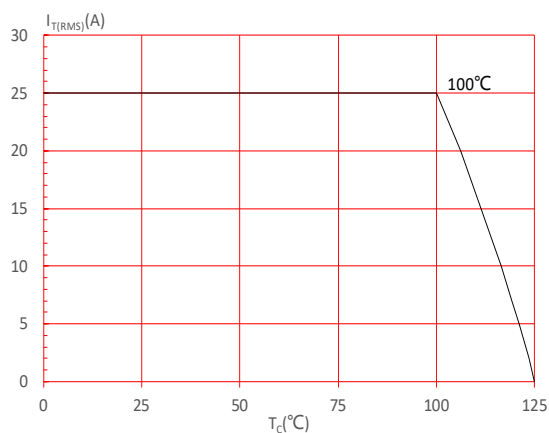
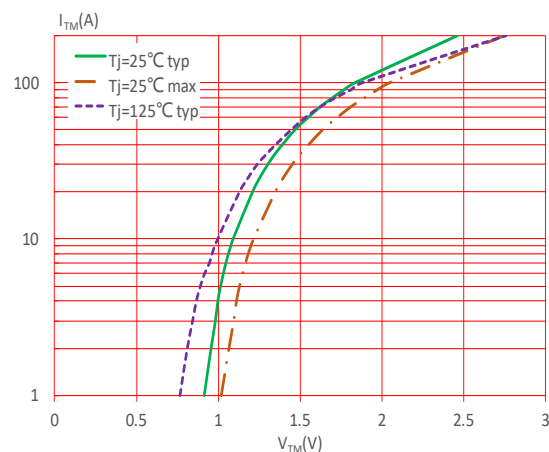
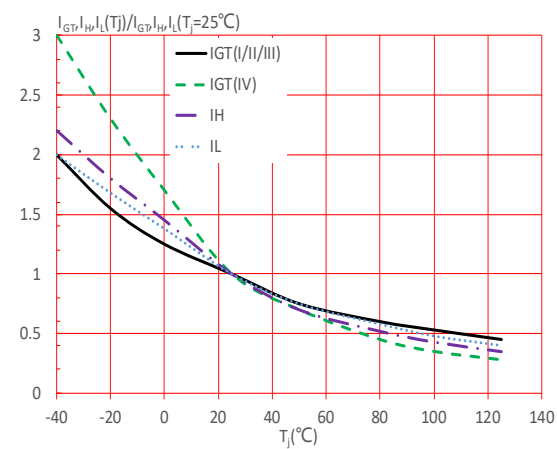
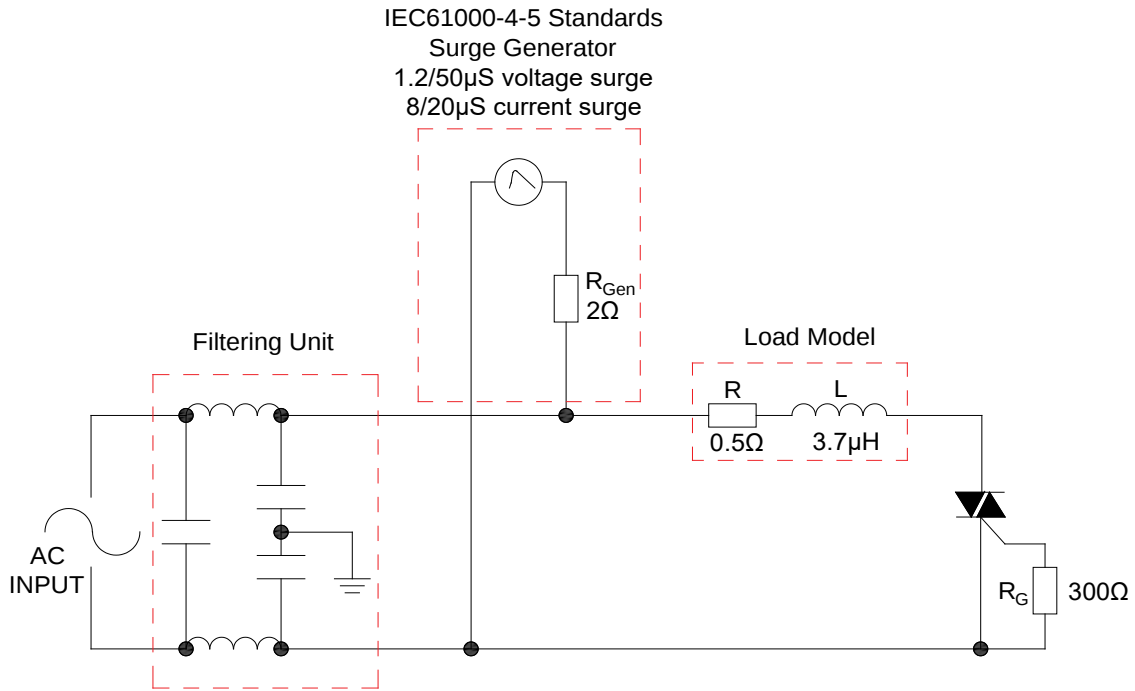
FIG.1: Maximum power dissipation versus RMS on-state current**FIG.3:** Surge peak on-state current versus number of cycles**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20ms$, and corresponding value of I^2t (I - II: $di/dt < 100A/\mu s$; III-IV: $di/dt < 50A/\mu s$)**FIG.2:** RMS on-state current versus case temperature**FIG.4:** On-state characteristics**FIG.6:** Relative variations of gate trigger current, holding current and latching current versus junction temperature

FIG.7: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie Microelectronics

ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		I - II - III	IV			
JST24C-600B	600	50	70	TO-220C	50	Tube

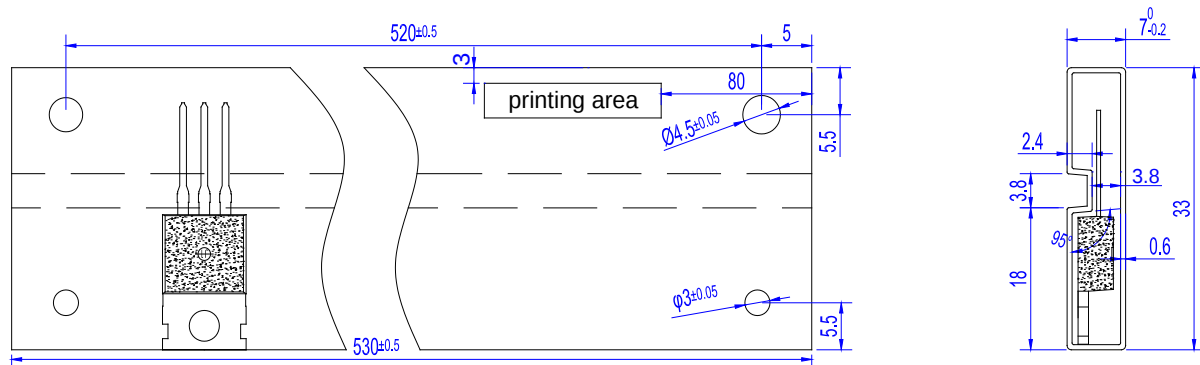
Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Oct.15, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

Technical drawing of a 3-pin plug with dimensions and labels:

- Top View:** Shows the circular base with a central hole and a diameter dimension of $\varnothing 3.45-3.65\text{mm}$.
- Front View:** Shows the plug body with dimensions:
 - E:** Total width of the plug body.
 - L:** Width of the top flange.
 - L3:** Height of the top flange.
 - H:** Total height of the plug body.
 - L1:** Height of the top flange.
 - K:** Height of the main body.
 - L2:** Width of the main body.
 - B:** Width of the bottom flange.
 - G:** Width of the bottom flange.
 - M:** Total width of the bottom flange.
- Bottom View:** Shows the bottom flange with dimensions:
 - E1:** Width of the bottom flange.
 - H1:** Height of the bottom flange.
- Side View:** Shows the plug body with dimensions:
 - A:** Total height of the plug body.
 - C2:** Height of the top flange.
 - D:** Height of the main body.
 - C3:** Height of the bottom flange.
 - C:** Width of the bottom flange.

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.40		4.60	0.173		0.181
B	0.70		0.90	0.028		0.035
C	0.45		0.60	0.018		0.024
C2	1.25		1.35	0.049		0.053
C3	2.20		2.60	0.087		0.102
D	8.90		9.90	0.350		0.390
E	9.90		10.30	0.390		0.406
E1	0.80		1.20	0.031		0.047
F	6.30		6.90	0.248		0.272
G	2.40		2.70	0.094		0.106
H	28.00		29.80	1.102		1.173
H1	2.15		2.55	0.085		0.100
I	4.70		5.10	0.185		0.201
K	9.90		10.30	0.390		0.406
L1	2.70		3.30	0.106		0.130
L2	1.14		1.70	0.045		0.067
L3	2.65		2.95	0.104		0.116
M	4.80		5.20	0.189		0.205



PACKAGE	OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON
TO-220C	TUBE	50	1,000	5,000

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.



is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright © 2025 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.