

JST136Q-800T 4A TRIAC

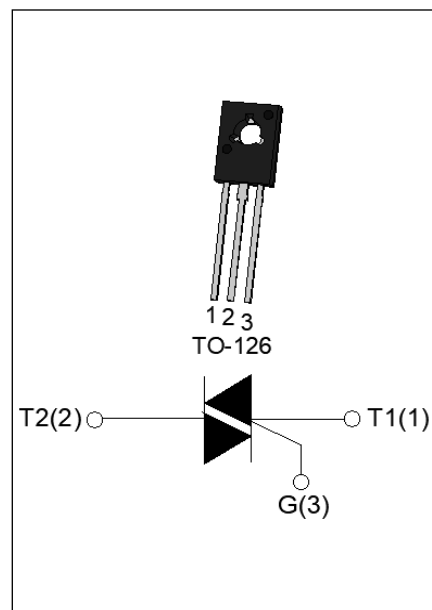
Rev.A.1.0

DESCRIPTION:

The JST136Q-800T triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. From T2 terminals to external heatsink. Package TO-126 is RoHS compliant.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	4	A
V_{DRM}/V_{RRM}	800	V
$I_{GT\ I}/II/III/IV$	5/5/5/5	mA


ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Storage junction temperature range		T _{stg}	-40-150	℃
Operating junction temperature range		T _j	-40-125	℃
Repetitive peak off-state voltage (T _j =25℃)		V _{DRM}	800	V
Repetitive peak reverse voltage (T _j =25℃)		V _{RRM}	800	V
RMS on-state current (T _c ≤83℃)		I _{T(RMS)}	4	A
Non repetitive surge peak on-state current (full cycle , t _p =20ms , T _j =25℃)		I _{TSM}	35	A
Non repetitive surge peak on-state current (full cycle , t _p =16.6ms , T _j =25℃)			38.5	
I ² t value for fusing (t _p =10ms , T _j =25℃)		I ² t	6.1	A ² s
Critical rate of rise of on-state current (I _G =2×I _{GT} , f=100Hz , T _j =125℃)	I - II	di/dt	50	A/μs
	III-IV		30	
Peak gate current (t _p =20μs , T _j =125℃)		I _{GM}	2	A
Average gate power dissipation (T _j =125℃)		P _{G(AV)}	0.5	W
Peak gate power		P _{GM}	5	W
Peak pulse voltage (T _j =25℃; non-repetitive,off-state;FIG.7)		V _{pp}	2.5	kV

ELECTRICAL CHARACTERISTICS ($T_j=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12\text{V}$ $R_L=33\Omega$	ALL	MAX.	5	mA
V_{GT}		ALL	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125^{\circ}\text{C}$ $R_L=3.3\text{k}\Omega$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	I -III	MAX.	10	mA
		II -IV		15	
I_H	$I_T=100\text{mA}$		MAX.	5	mA
dV/dt	$V_D=540\text{V}$ Gate Open $T_j=110^{\circ}\text{C}$		MIN.	20	$\text{V}/\mu\text{s}$
$(dV/dt)_c$	$(dI/dt)_c=1.8\text{A/ms}$, $T_j=110^{\circ}\text{C}$		MIN.	1	$\text{V}/\mu\text{s}$
t_{on}	$I_G=10\text{mA}$ $I_A=200\text{mA}$ $I_R=20\text{mA}$ $T_j=25^{\circ}\text{C}$	TYP.		1	μs
t_{off}				12	

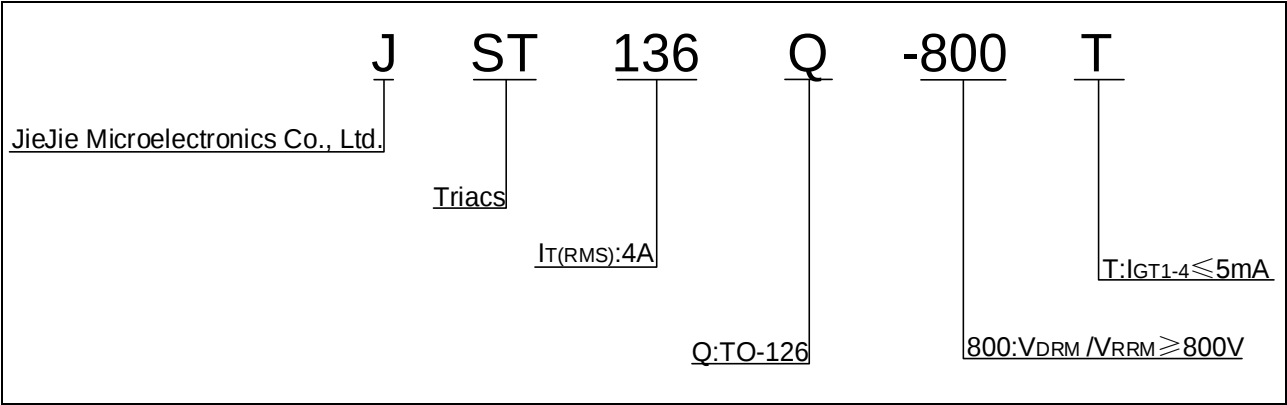
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=5\text{A}$ $t_p=380\mu\text{s}$	$T_j=25^{\circ}\text{C}$	1.7	V
V_{TO}	Threshold voltage	$T_j=125^{\circ}\text{C}$	0.94	V
R_D	Dynamic resistance	$T_j=125^{\circ}\text{C}$	124	$\text{m}\Omega$
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25^{\circ}\text{C}$	5	μA
I_{RRM}		$T_j=125^{\circ}\text{C}$	0.4	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	6.5	$^{\circ}\text{C}/\text{W}$
$R_{th(j-a)}$	junction to ambient (AC)	150	$^{\circ}\text{C}/\text{W}$

ORDERING INFORMATION



MARKING

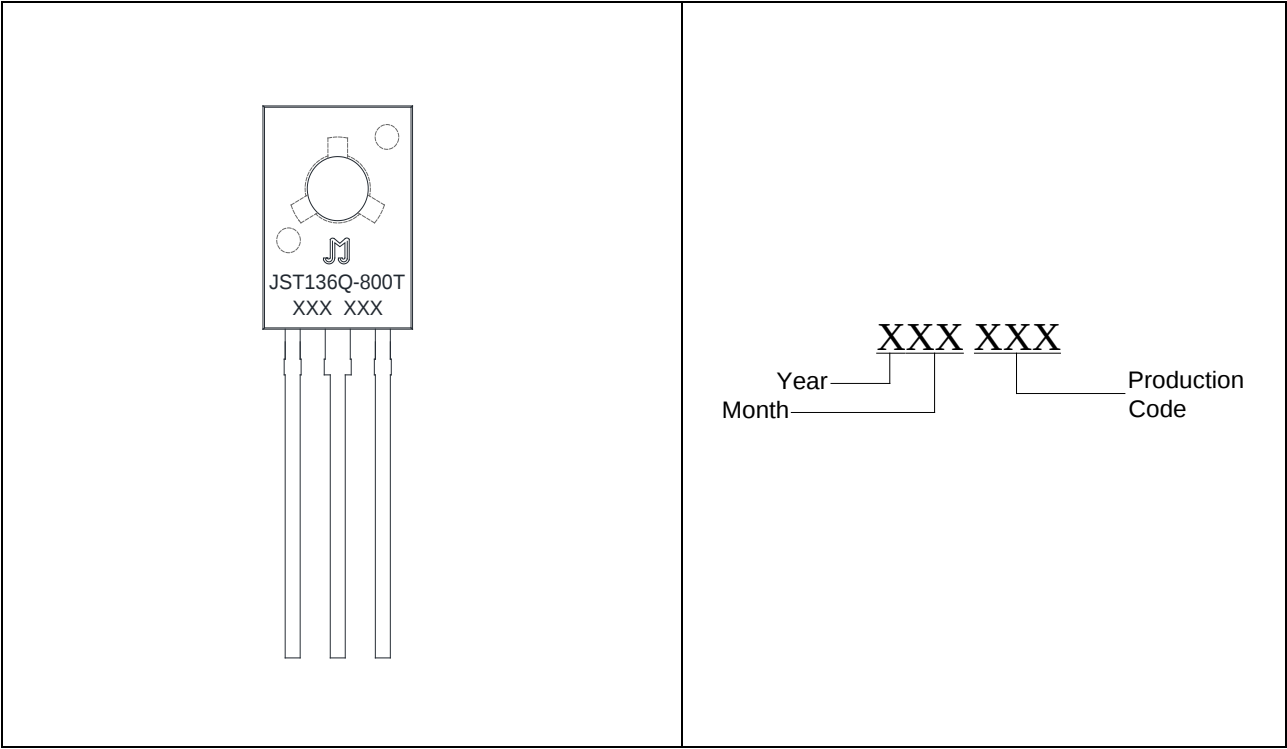


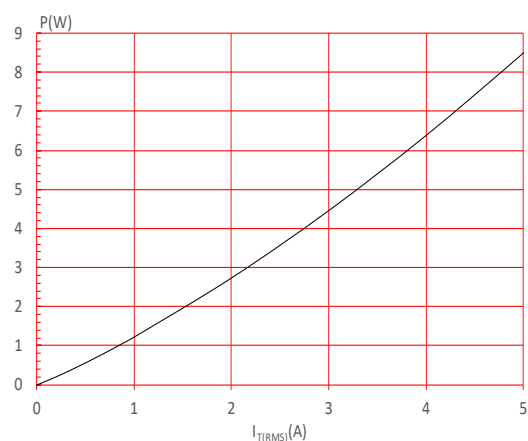
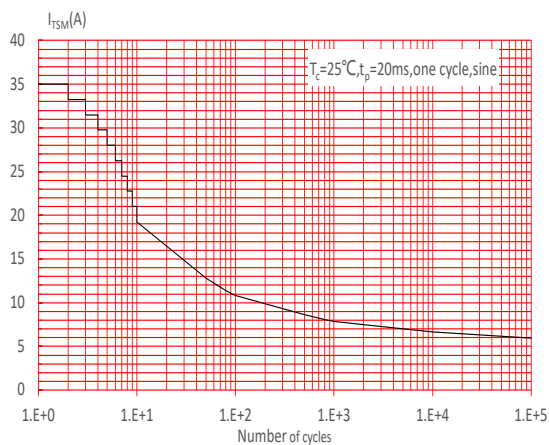
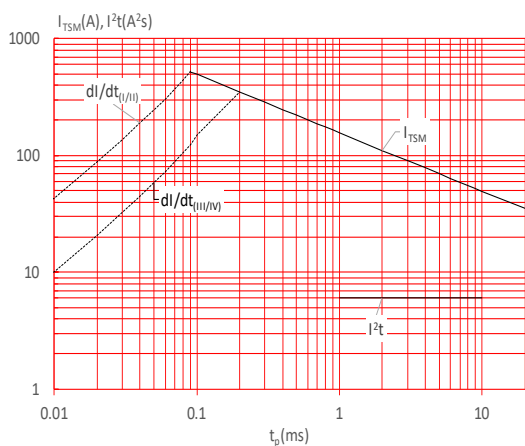
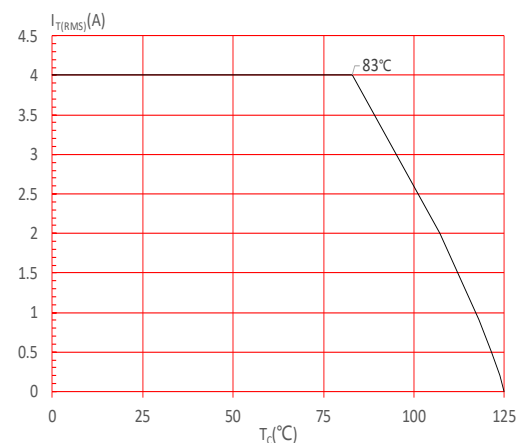
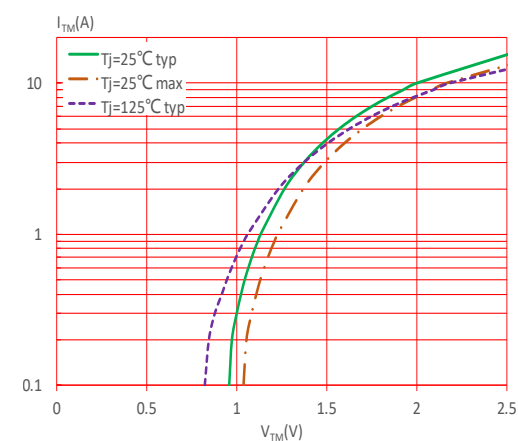
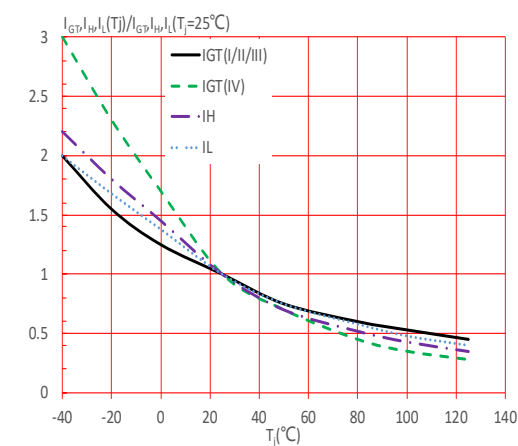
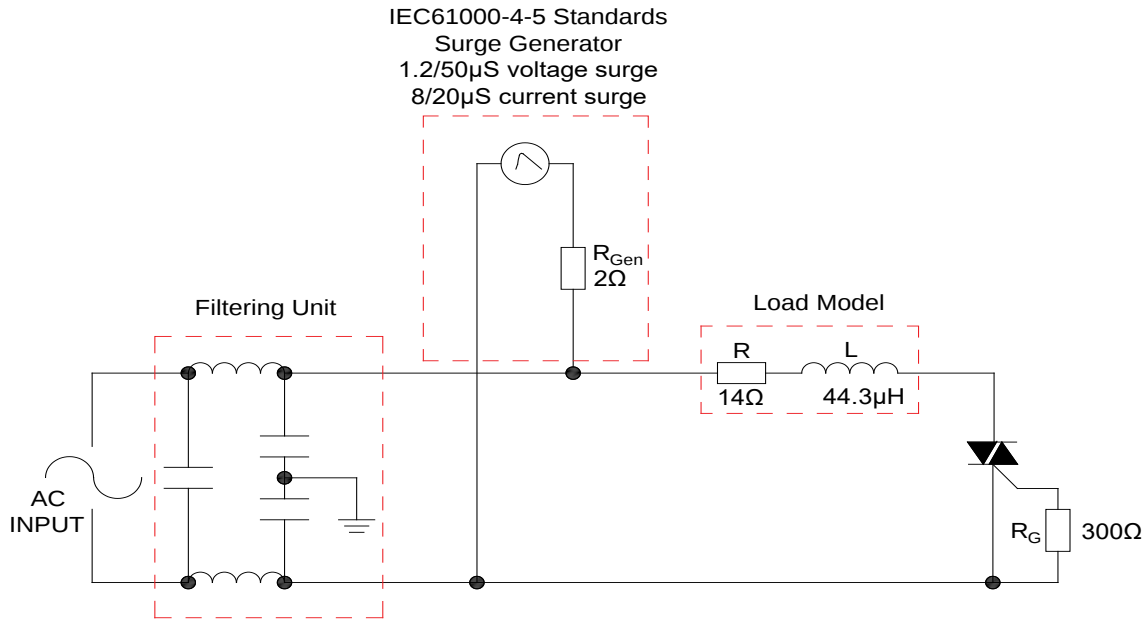
FIG.1: Maximum power dissipation versus RMS on-state current**FIG.3:** Surge peak on-state current versus number of cycles**FIG.5:** Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20ms$, and corresponding value of I^2t (I - II: $di/dt < 50A/\mu s$; III-IV: $di/dt < 30A/\mu s$)**FIG.2:** RMS on-state current versus case temperature**FIG.4:** On-state characteristics**FIG.6:** Relative variations of gate trigger current, holding current and latching current versus junction temperature

FIG.7: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



LEAD FORMING AND SOLDERING

Refer to the application note “Assembly Instructions for Thyristors in Through-hole Package” released by JieJie Microelectronics.

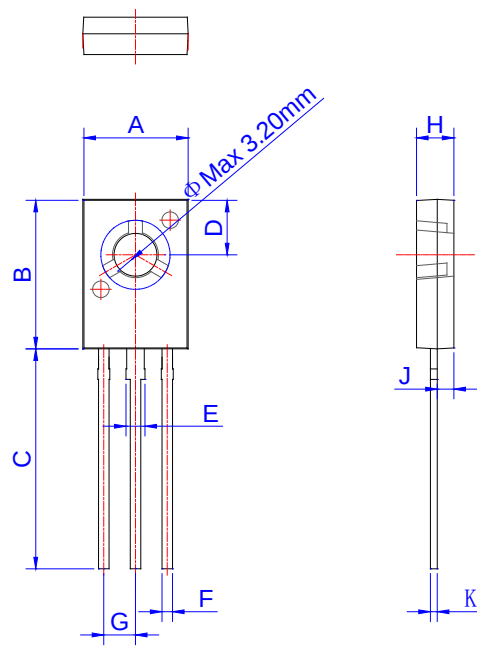
ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)	Package	Base qty. (pcs)	Delivery mode
		I - II - III - IV			
JST136Q-800T	800	5	TO-126	500	Bulk Pack

Document Revision History

Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	7.40		7.80	0.291		0.307
B	10.6		11.2	0.417		0.441
C	15.3		16.3	0.602		0.642
D	3.90		4.10	0.154		0.161
E	1.17		1.47	0.046		0.058
F	0.66		0.86	0.026		0.034
G	2.15		2.45	0.085		0.096
H	2.50		2.90	0.098		0.114
J	1.10		1.50	0.043		0.059
K	0.45		0.60	0.018		0.024

DELIVERY MODE

PACKAGE	OUTLINE	BAG (PCS)	INNER BOX (PCS)	CARTON BOX (PCS)
TO-126	Bulk Pack	500	2,000	10,000

Information furnished in this document is believed to be accurate and reliable. However, Jiangsu JieJie Microelectronics Co., Ltd assumes no responsibility for the consequences of use without consideration for such information nor use beyond it. Information mentioned in this document is subject to change without notice, apart from that when an agreement is signed, Jiangsu JieJie complies with the agreement.

Products and information provided in this document have no infringement of patents. Jiangsu JieJie assumes no responsibility for any infringement of other rights of third parties which may result from the use of such products and information. This document supersedes and replaces all information previously supplied.



is a registered trademark of Jiangsu JieJie Microelectronics Co., Ltd.

Copyright © 2023 Jiangsu JieJie Microelectronics Co., Ltd. All rights reserved.