

JST12E-800B 12A TRIAC

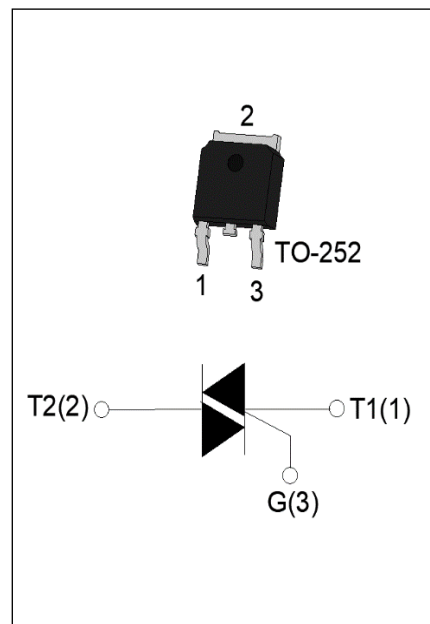
Rev.A.1.1

DESCRIPTION:

The JST12E-800B triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. Package TO-263 is RoHS compliant.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	12	A
V_{DRM}/V_{RRM}	800	V
$I_{GT\ I/II/III/IV}$	50/50/50/70	mA


ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Storage junction temperature range		T _{stg}	-40-150	℃
Operating junction temperature range		T _j	-40-125	℃
Repetitive peak off-state voltage (T _j =25℃)		V _{DRM}	800	V
Repetitive peak reverse voltage (T _j =25℃)		V _{RRM}	800	V
RMS on-state current (T _c ≤103℃)		I _{T(RMS)}	12	A
Non repetitive surge peak on-state current (full cycle , t _p =20ms , T _j =25℃)		I _{TSM}	120	A
Non repetitive surge peak on-state current (full cycle , t _p =16.6ms , T _j =25℃)			132	
I ² t value for fusing (t _p =10ms , T _j =25℃)		I ² t	72	A ² s
Critical rate of rise of on-state current (I _G =2×I _{GT} , f=100Hz , T _j =125℃)	I - II	dI/dt	100	A/μs
	III-IV		50	
Peak gate current (t _p =20μs , T _j =125℃)		I _{GM}	4	A
Average gate power dissipation (T _j =125℃)		P _{G(AV)}	0.5	W
Peak gate power		P _{GM}	10	W
Peak pulse voltage (T _j =25℃; non-repetitive,off-state;FIG.8)		V _{pp}	2	kV

ELECTRICAL CHARACTERISTICS ($T_j=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12\text{V}$ $R_L=33\Omega$	I - II - III	MAX.	50	mA
		IV		70	
V_{GT}		ALL	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125^{\circ}\text{C}$ $R_L=3.3\text{k}\Omega$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	I - III - IV	MAX.	50	mA
		II		100	
I_H	$I_T=500\text{mA}$		MAX.	60	mA
dV/dt	$V_D=540\text{V}$ Gate Open $T_j=125^{\circ}\text{C}$		MIN.	1000	$\text{V}/\mu\text{s}$
$(dV/dt)_c$	$(dI/dt)_c=5.3\text{A/ms}$, $T_j=125^{\circ}\text{C}$		MIN.	12	$\text{V}/\mu\text{s}$
t_{on}	$I_G=80\text{mA}$ $I_A=400\text{mA}$ $I_R=40\text{mA}$ $T_j=25^{\circ}\text{C}$		TYP.	5	μs
t_{off}				30	

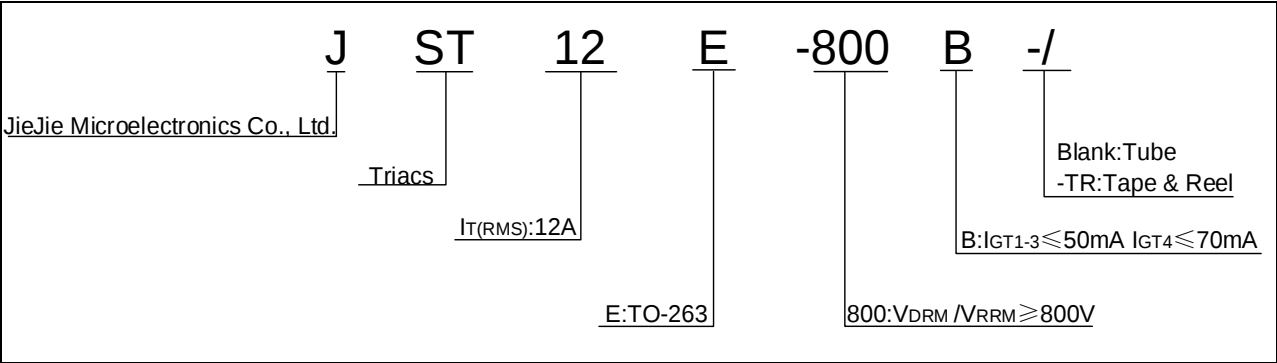
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=17\text{A}$ $t_p=380\mu\text{s}$	$T_j=25^{\circ}\text{C}$	1.5	V
V_{TO}	Threshold voltage	$T_j=125^{\circ}\text{C}$	0.77	V
R_D	Dynamic resistance	$T_j=125^{\circ}\text{C}$	35	$\text{m}\Omega$
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25^{\circ}\text{C}$	5	μA
I_{RRM}		$T_j=125^{\circ}\text{C}$	0.5	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	1.3	$^{\circ}\text{C}/\text{W}$
$R_{th(j-a)}$	junction to ambient (AC, in free air, $S=2\text{cm}^2$)	45	$^{\circ}\text{C}/\text{W}$

ORDERING INFORMATION



MARKING

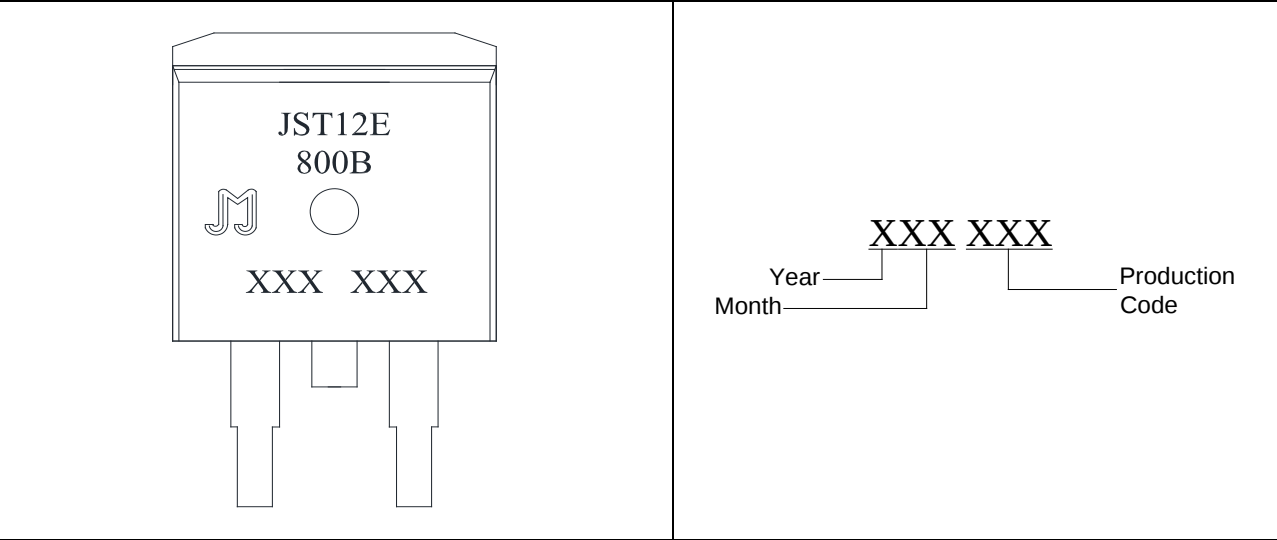


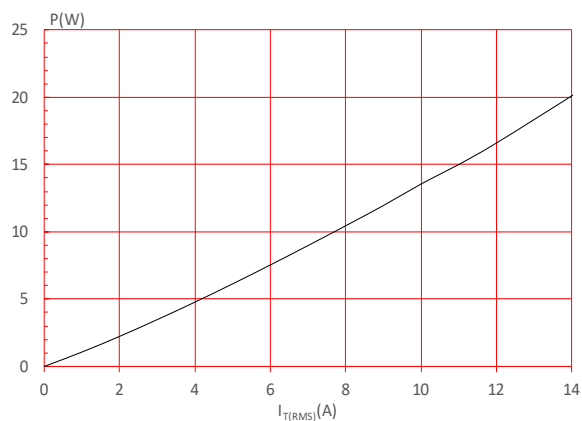
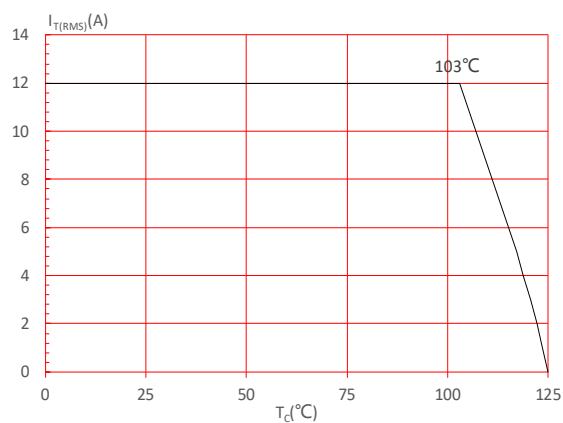
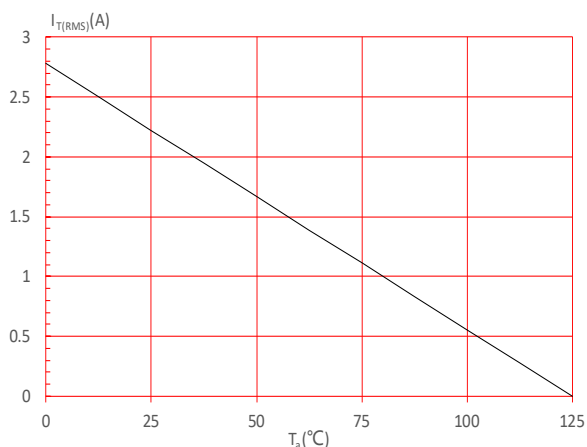
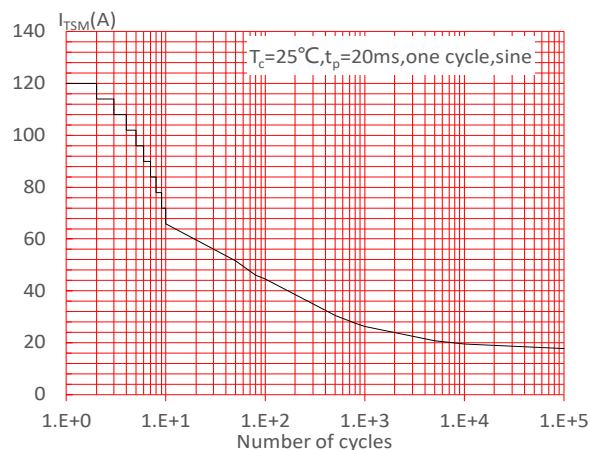
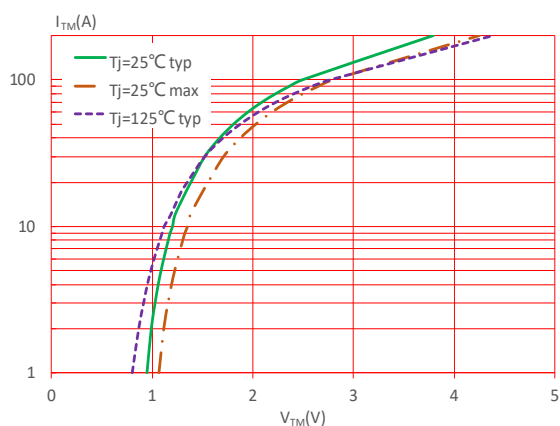
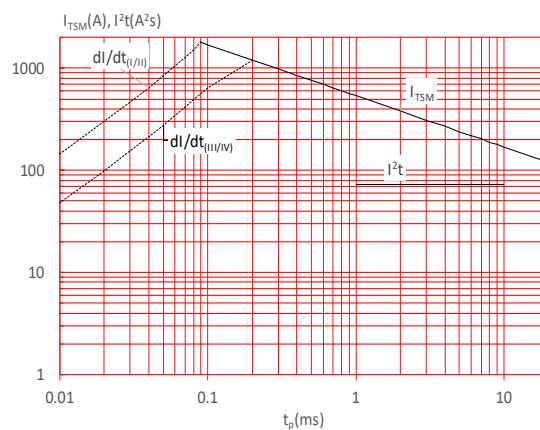
FIG.1: Maximum power dissipation versus RMS on-state current

FIG.2: RMS on-state current versus case temperature

FIG.3: RMS on-state current versus ambient temperature (printed circuit board FR4,copper thickness:35μm)(full cycle)

FIG.4: Surge peak on-state current versus number of cycles

FIG.5: On-state characteristics

FIG.6: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20ms$, and corresponding value of I^2t (I - II: $di/dt < 100A/μs$; III-IV: $di/dt < 50A/μs$)


FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

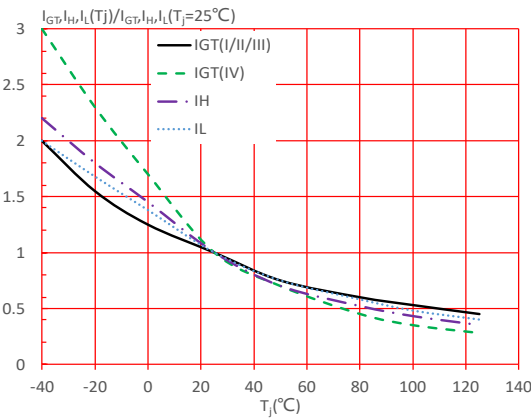
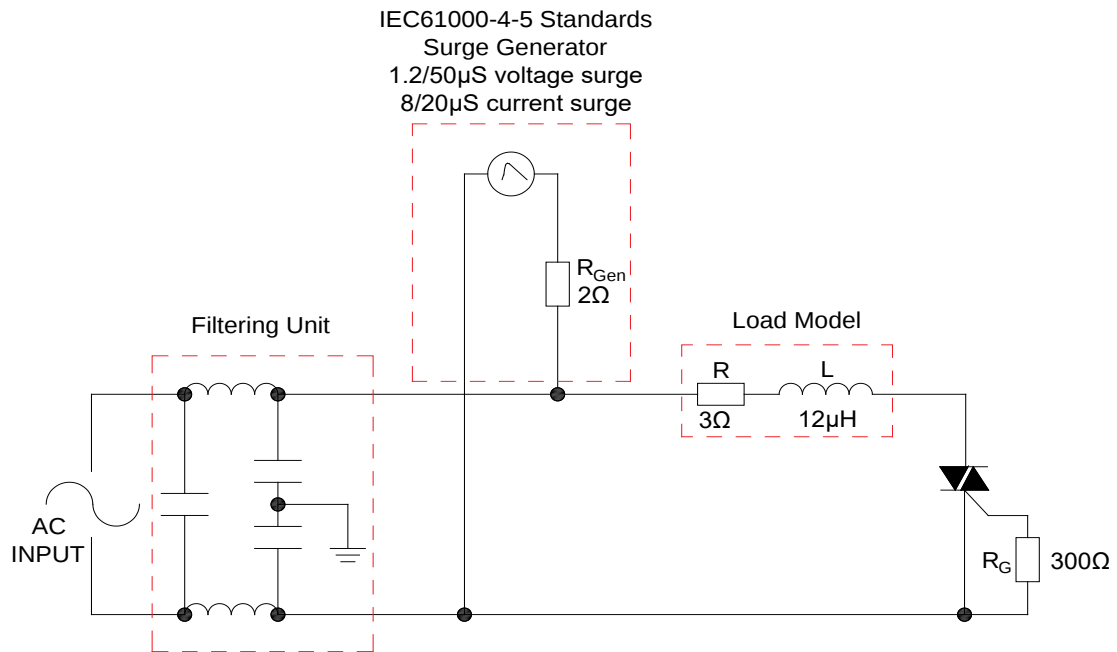
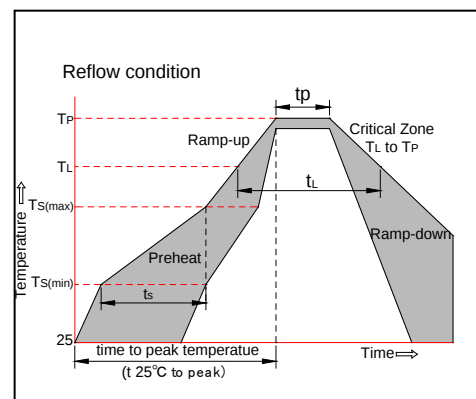


FIG.8: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



SOLDERING PARAMETERS

Reflow Condition		Pb-Free assembly (see figure at right)
Pre Heat	-Temperature Min ($T_{s(min)}$)	+150 $^{\circ}$ C
	-Temperature Max($T_{s(max)}$)	+200 $^{\circ}$ C
	-Time (Min to Max) (ts)	60-180 secs.
Average ramp up rate (Liquidus Temp (T_L) to peak)		3 $^{\circ}$ C/sec. Max
$T_{s(max)}$ to T_L - Ramp-up Rate		3 $^{\circ}$ C/sec. Max
Reflow	-Temperature(T_L) (Liquidus)	+217 $^{\circ}$ C
	-Temperature(t_L)	60-150 secs.
Peak Temp (T_p)		+260(+0/-5) $^{\circ}$ C
Time within 5 $^{\circ}$ C of actual Peak Temp (t_p)		20-40secs.
Ramp-down Rate		6 $^{\circ}$ C/sec. Max
Time 25 $^{\circ}$ C to Peak Temp (T_p)		8 min. Max
Do not exceed		+260 $^{\circ}$ C



ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		I - II - III	IV			
JST12E-800B	800	50	70	TO-263	50	Tube
JST12E-800B-TR					800	Tape & Reel

Document Revision History

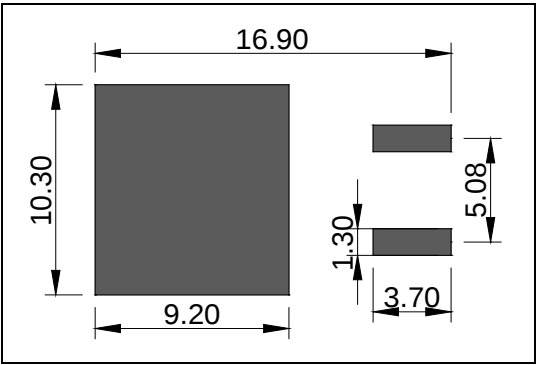
Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Oct.20, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

PACKAGE MECHANICAL DATA

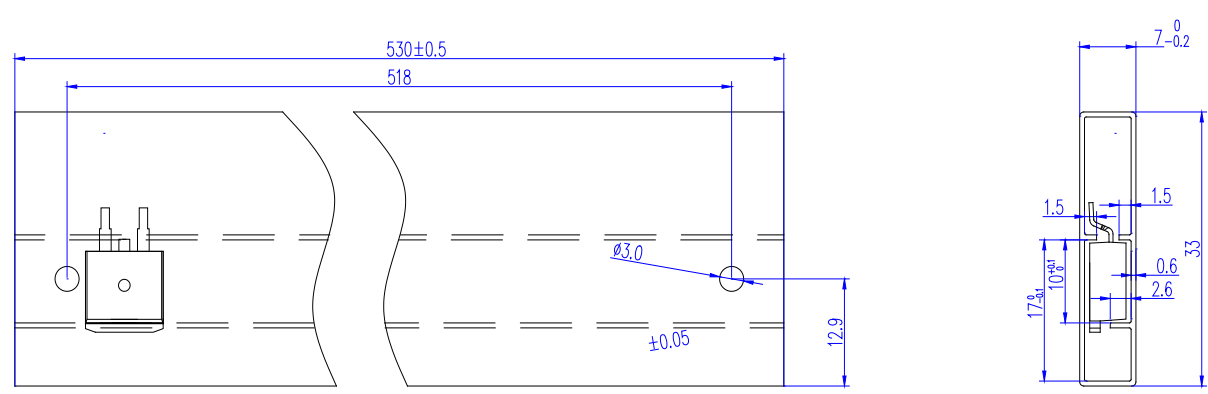


Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	9.90		10.20	0.390		0.402
A1	0.80		1.20	0.031		0.047
B	14.70		15.80	0.579		0.622
B1	2.15		2.55	0.085		0.100
C	9.40		9.60	0.370		0.378
C1	2.40		2.80	0.094		0.110
D	2.40		2.70	0.094		0.106
E	1.20		1.50	0.047		0.059
F	0.75		0.85	0.029		0.033
G	1.00		1.50	0.039		0.059
H	4.40		4.70	0.173		0.185
I	1.80		2.20			
J	2.30		2.70	0.091		0.106
K	0.38		0.55	0.015		0.022
L	0	0.10	0.25	0	0.004	0.010
L1	4.65		5.05	0.183		0.199
L2	1.90			0.075		
M	1.25		1.35	0.049		0.053
N	4.80		5.20			
O	1.30		1.50			
P	1.20		1.50	0.047		0.059

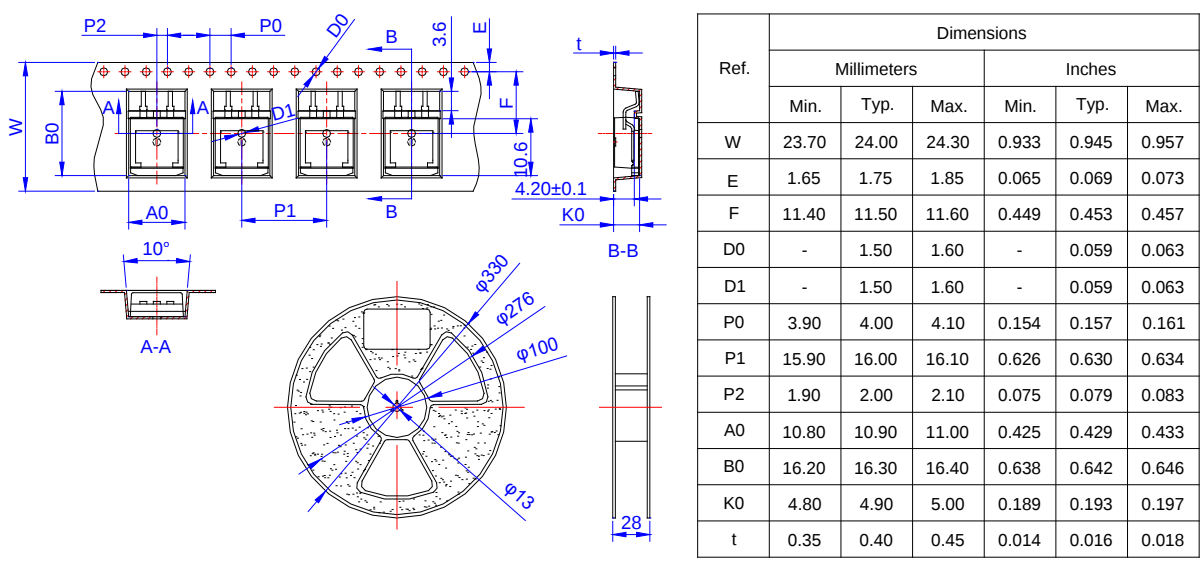
FOOTPRINT-TO-263 (dimensions in mm)



DELIVERY MODE



PACKAGE	OUTLINE	TUBE (PCS)	INNER BOX (PCS)	PER CARTON
TO-263	TUBE	50	1,000	5,000



PACKAGE	OUTLINE	REEL (PCS)	PER CARTON (PCS)	TAPE & REEL
TO-263	TAPING	800	4,000	13 inch

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