

JST08K-600C 8A TRIAC

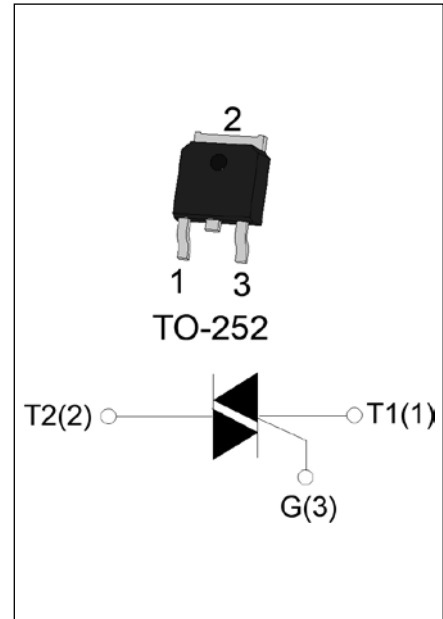
Rev.A.1.1

DESCRIPTION:

The JST08K-600C triac is suitable for general purpose AC switching. It can be used as an ON/OFF function in applications such as heating regulation, induction motor starting circuits, for phase control operation in light dimmers, motor speed controllers. Package TO-252 is RoHS compliant.

MAIN FEATURES

Symbol	Value	Unit
$I_{T(RMS)}$	8	A
V_{DRM}/V_{RRM}	600	V
$I_{GT\ I / II / III / IV}$	25/25/25/50	mA


ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value	Unit
Storage junction temperature range		T _{stg}	-40-150	℃
Operating junction temperature range		T _j	-40-125	℃
Repetitive peak off-state voltage (T _j =25℃)		V _{DRM}	600	V
Repetitive peak reverse voltage (T _j =25℃)		V _{RRM}	600	V
RMS on-state current (T _c ≤91℃)		I _{T(RMS)}	8	A
Non repetitive surge peak on-state current (full cycle , t _p =20ms , T _j =25℃)		I _{TSM}	80	A
Non repetitive surge peak on-state current (full cycle , t _p =16.6ms , T _j =25℃)			88	
I ² t value for fusing (t _p =10ms , T _j =25℃)		I ² t	32	A ² s
Critical rate of rise of on-state current (I _G =2×I _{GT} , f=100Hz , T _j =125℃)	I - II	dI/dt	80	A/μs
	III-IV		40	
Peak gate current (t _p =20μs , T _j =125℃)		I _{GM}	4	A
Average gate power dissipation (T _j =125℃)		P _{G(AV)}	0.5	W
Peak gate power		P _{GM}	10	W
Peak pulse voltage (T _j =25℃ ; non-repetitive,off-state;FIG.8)		V _{PP}	1.5	kV

ELECTRICAL CHARACTERISTICS ($T_j=25^{\circ}\text{C}$ unless otherwise specified)

Symbol	Test Condition	Quadrant	Value		Unit
I_{GT}	$V_D=12\text{V}$ $R_L=33\Omega$	I - II - III	MAX.	25	mA
		IV		50	
V_{GT}		ALL	MAX.	1	V
V_{GD}	$V_D=V_{DRM}$ $T_j=125^{\circ}\text{C}$ $R_L=3.3\text{k}\Omega$	ALL	MIN.	0.2	V
I_L	$I_G=1.2I_{GT}$	I - III - IV	MAX.	40	mA
		II		80	
I_H	$I_T=200\text{mA}$		MAX.	30	mA
dV/dt	$V_D=400\text{V}$ Gate Open $T_j=125^{\circ}\text{C}$		MIN.	800	$\text{V}/\mu\text{s}$
$(dV/dt)_c$	$(dI/dt)_c=3.5\text{A/ms}$, $T_j=125^{\circ}\text{C}$		MIN.	6	$\text{V}/\mu\text{s}$
t_{on}	$I_G=80\text{mA}$ $I_A=400\text{mA}$ $I_R=40\text{mA}$ $T_j=25^{\circ}\text{C}$		TYP.	5	μs
t_{off}				30	

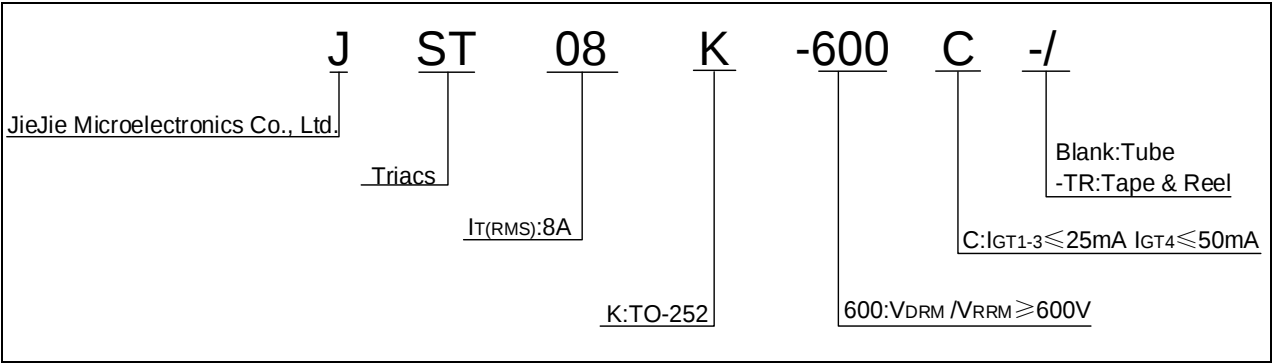
STATIC CHARACTERISTICS

Symbol	Parameter		Value(MAX.)	Unit
V_{TM}	$I_{TM}=11\text{A}$ $t_p=380\mu\text{s}$	$T_j=25^{\circ}\text{C}$	1.5	V
V_{TO}	Threshold voltage	$T_j=125^{\circ}\text{C}$	0.8	V
R_D	Dynamic resistance	$T_j=125^{\circ}\text{C}$	44	$\text{m}\Omega$
I_{DRM}	$V_D=V_{DRM}$ $V_R=V_{RRM}$	$T_j=25^{\circ}\text{C}$	5	μA
I_{RRM}		$T_j=125^{\circ}\text{C}$	0.25	mA

THERMAL RESISTANCES

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	junction to case (AC)	3	$^{\circ}\text{C}/\text{W}$
$R_{th(j-a)}$	junction to ambient (AC)	100	$^{\circ}\text{C}/\text{W}$

ORDERING INFORMATION



MARKING

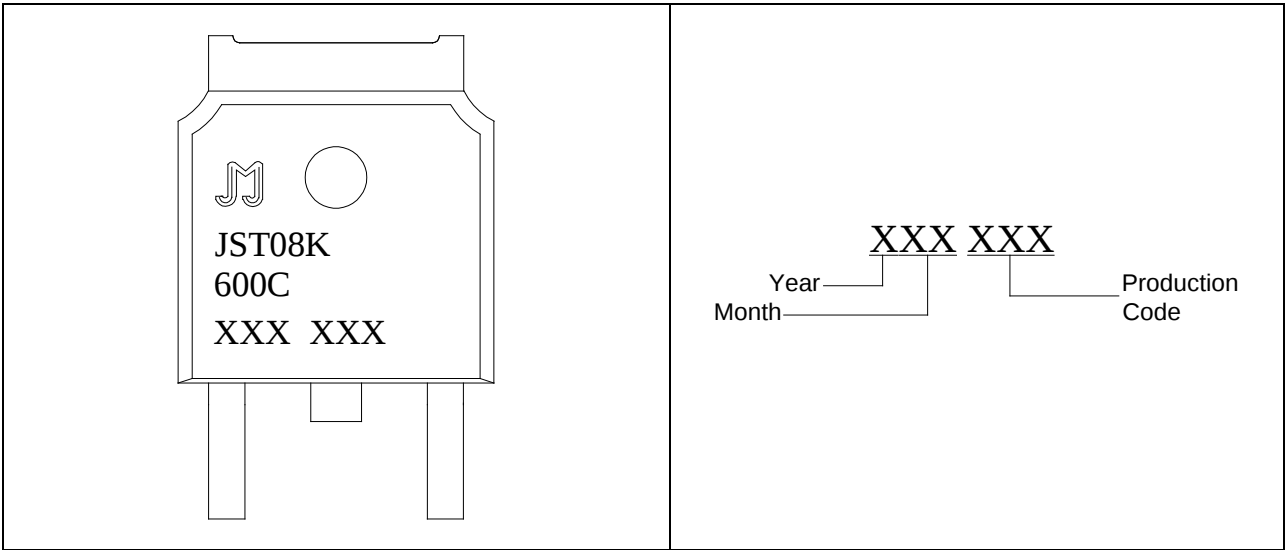


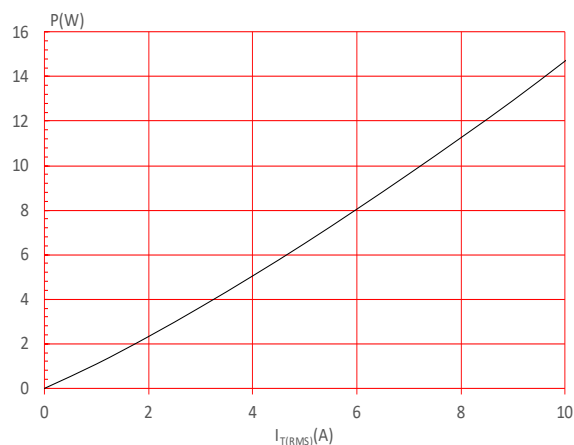
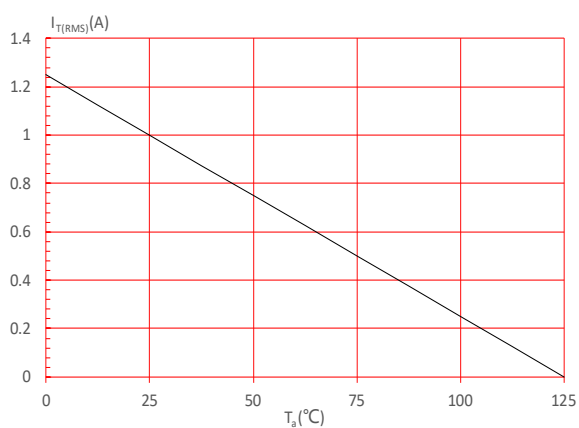
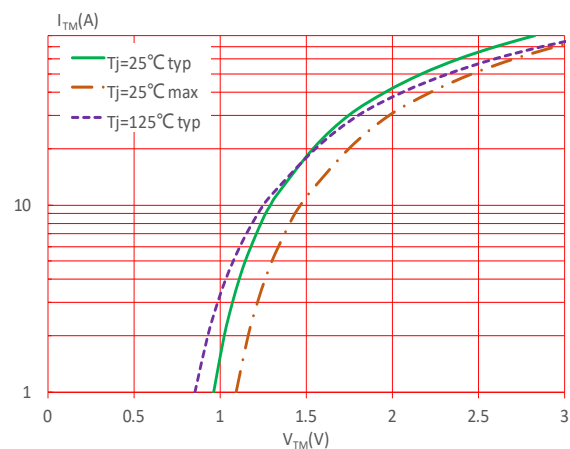
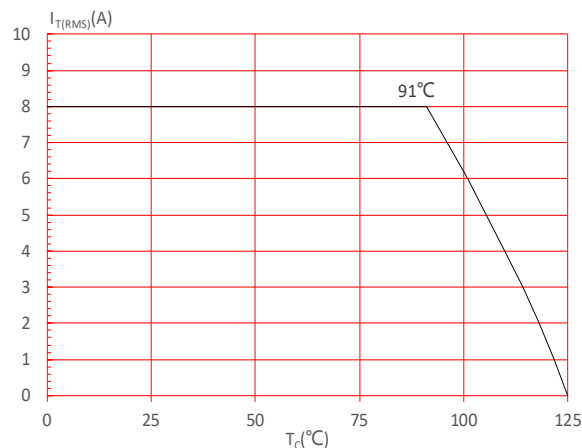
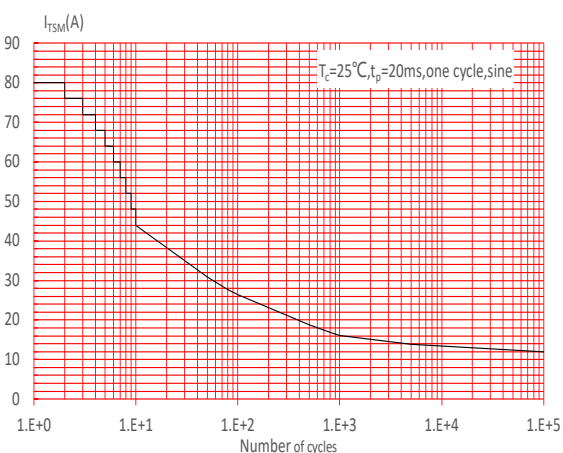
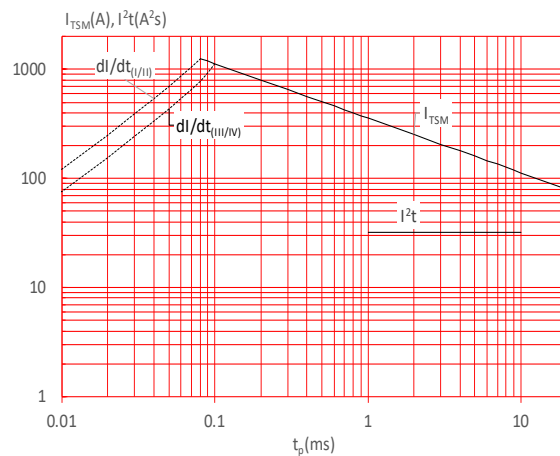
FIG.1: Maximum power dissipation versus RMS on-state current

FIG.3: RMS on-state current versus ambient temperature (printed circuit board FR4, copper thickness: 35 μm) (full cycle)

FIG.5: On-state characteristics

FIG.2: RMS on-state current versus case temperature

FIG.4: Surge peak on-state current versus number of cycles

FIG.6: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 20ms$, and corresponding value of I^2t (I - II: $di/dt < 80A/\mu s$; III-IV: $di/dt < 40A/\mu s$)


FIG.7: Relative variations of gate trigger current, holding current and latching current versus junction temperature

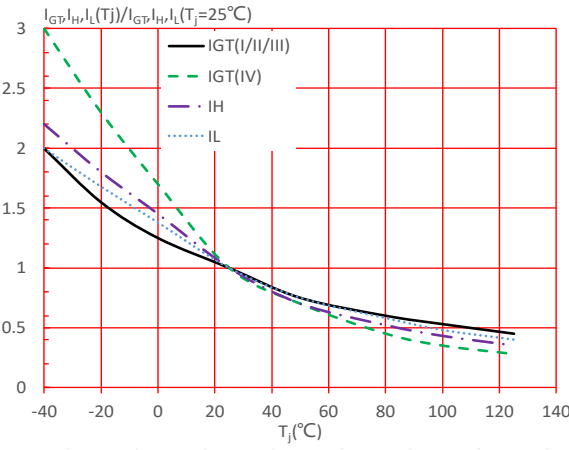
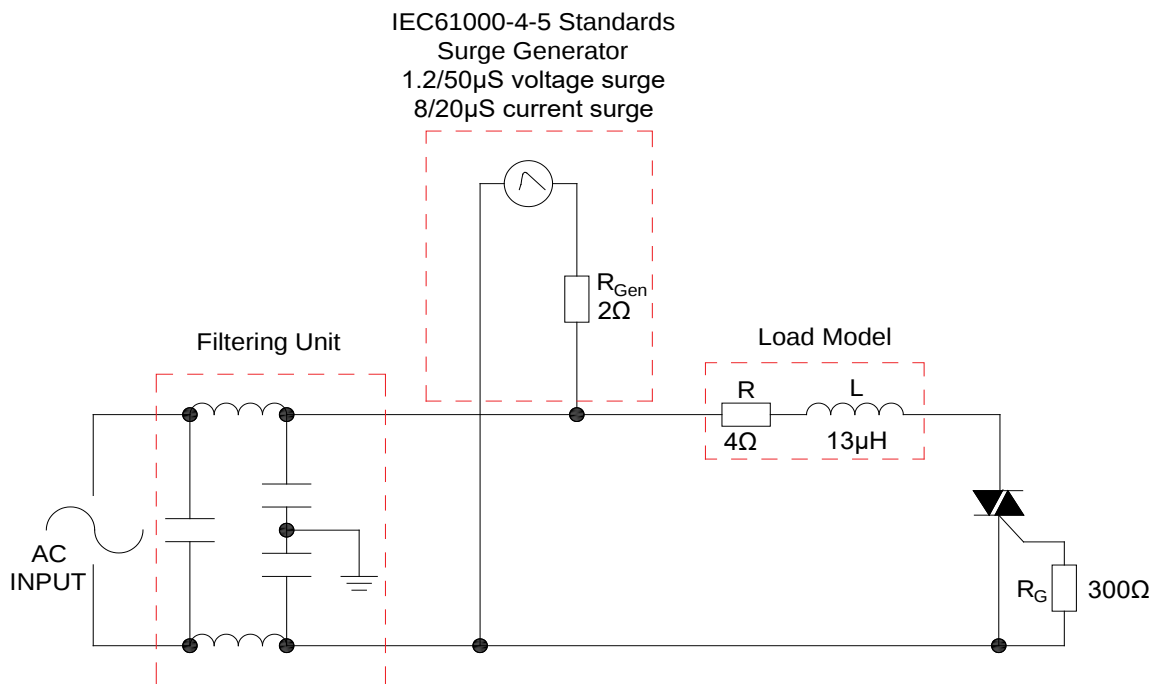
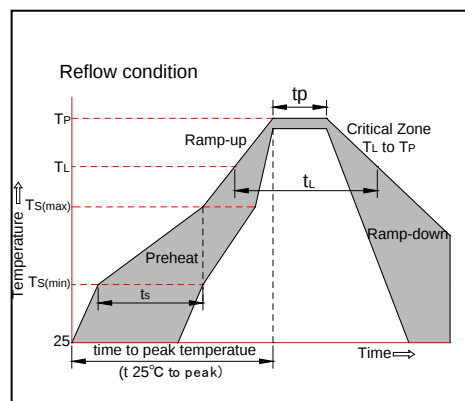


FIG.8: Test circuit for inductive and resistive loads to IEC-61000-4-5 standards



SOLDERING PARAMETERS

Reflow Condition		Pb-Free assembly (see figure at right)
Pre Heat	-Temperature Min ($T_{s(min)}$)	+150 $^{\circ}$ C
	-Temperature Max($T_{s(max)}$)	+200 $^{\circ}$ C
	-Time (Min to Max) (t_s)	60-180 secs.
Average ramp up rate (Liquidus Temp (T_L) to peak)		3 $^{\circ}$ C/sec. Max
$T_{s(max)}$ to T_L - Ramp-up Rate		3 $^{\circ}$ C/sec. Max
Reflow	-Temperature(T_L) (Liquidus)	+217 $^{\circ}$ C
	-Temperature(t_L)	60-150 secs.
Peak Temp (T_p)		+260(+0/-5) $^{\circ}$ C
Time within 5 $^{\circ}$ C of actual Peak Temp (t_p)		20-40secs.
Ramp-down Rate		6 $^{\circ}$ C/sec. Max
Time 25 $^{\circ}$ C to Peak Temp (T_P)		8 min. Max
Do not exceed		+260 $^{\circ}$ C



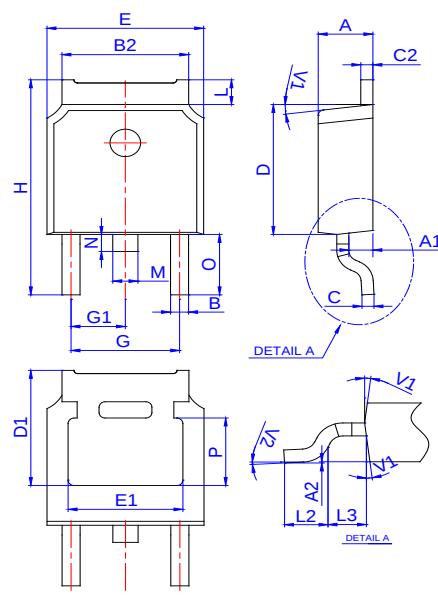
ORDERING INFORMATION

Order code	Voltage $V_{\text{DRM}}/V_{\text{RRM}}$ (V)	IGT(mA)		Package	Base qty. (pcs)	Delivery mode
		I - II - III	IV			
JST08K-600C	600	25	50	TO-252	80	Tube
JST08K-600C-TR					2,500	Tape & Reel

Document Revision History

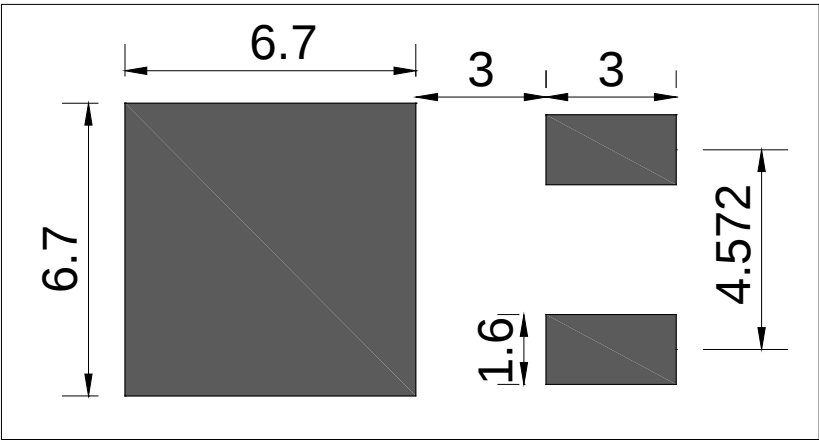
Date	Revision	Changes
Apr.14, 2023	A.1.0	Last updated
Oct.22, 2025	A.1.1	Revise PACKAGE MECHANICAL DATA

PACKAGE MECHANICAL DATA



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A1	0.80		1.20	0.031		0.047
A2	0		0.15	0		0.006
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
G1	2.18		2.38	0.086		0.094
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
L3	1.10		1.50	0.043		0.059
M	0.65		0.95	0.026		0.037
N	0.65		0.95	0.026		0.037
O	2.80		3.20	0.110		0.126
P	3.10		3.30	0.122		0.130
V1		7°			7°	
V2	0°		6°	0°		6°

FOOTPRINT-TO-252 (dimensions in mm)



Technical drawing of a mechanical part, showing front and side views with dimensions.

Front View (Left):

- Overall length: 548 ± 1
- Top flange width: 4
- Top flange thickness: 6
- Top flange hole diameter: $\phi 3 \pm 0.05$
- Bottom flange thickness: 1.20

Side View (Right):

- Overall height: 19.75
- Top flange thickness: 1.50 ± 0.10
- Top flange hole diameter: 1.90 ± 0.10
- Top flange hole offset: 1.40 ± 0.10
- Top flange hole diameter: 2.40
- Top flange hole offset: ± 0.10
- Top flange hole diameter: 1.85 ± 0.10
- Top flange hole offset: 2.2 ± 0.10
- Top flange hole diameter: 5.25 ± 0.10
- Top flange hole offset: 7.25 ± 0.10
- Top flange hole diameter: 0.50
- Top flange hole offset: 1.20

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
W	15.90	16.00	16.10	0.626	0.630	0.634
E	1.65	1.75	1.85	0.065	0.069	0.073
F	7.40	7.50	7.60	0.291	0.295	0.299
D0	1.50	1.55	1.60	0.059	0.060	0.063
D1	1.50	—	—	0.059	—	—
P0	3.90	4.00	4.10	0.154	0.157	0.161
P1	7.90	8.00	8.10	0.311	0.315	0.319
P2	1.90	2.00	2.10	0.075	0.079	0.083
10P0	39.80	40.00	40.20	1.567	1.575	1.583
A0	6.80	6.90	7.00	0.267	0.272	0.276
B0	10.40	10.50	10.60	0.408	0.413	0.417
K0	2.60	2.70	2.80	0.102	0.106	0.110
T	0.25	0.30	0.35	0.010	0.012	0.014
t1	0.10	—	—	0.004	—	—

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